

三

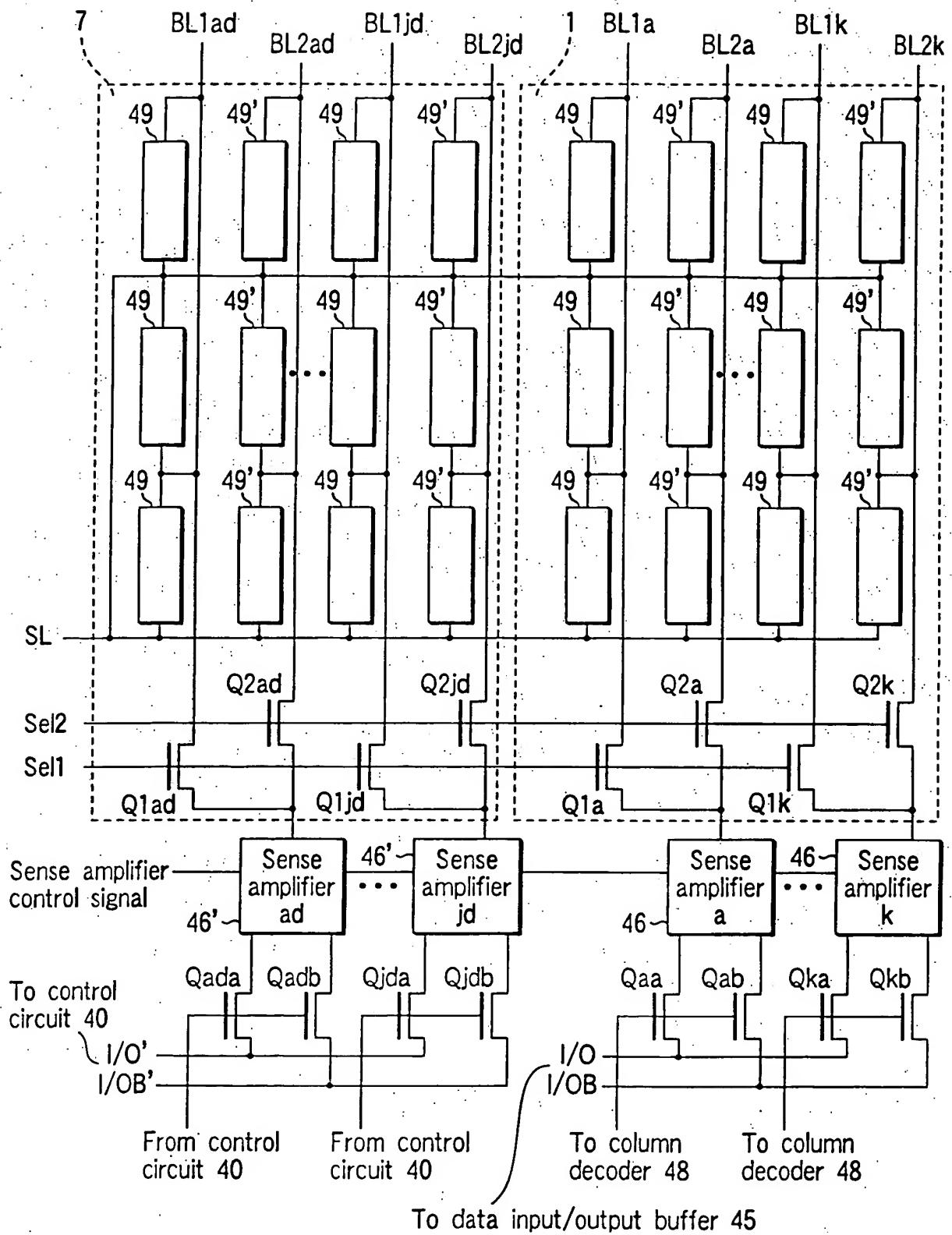


FIG. 2

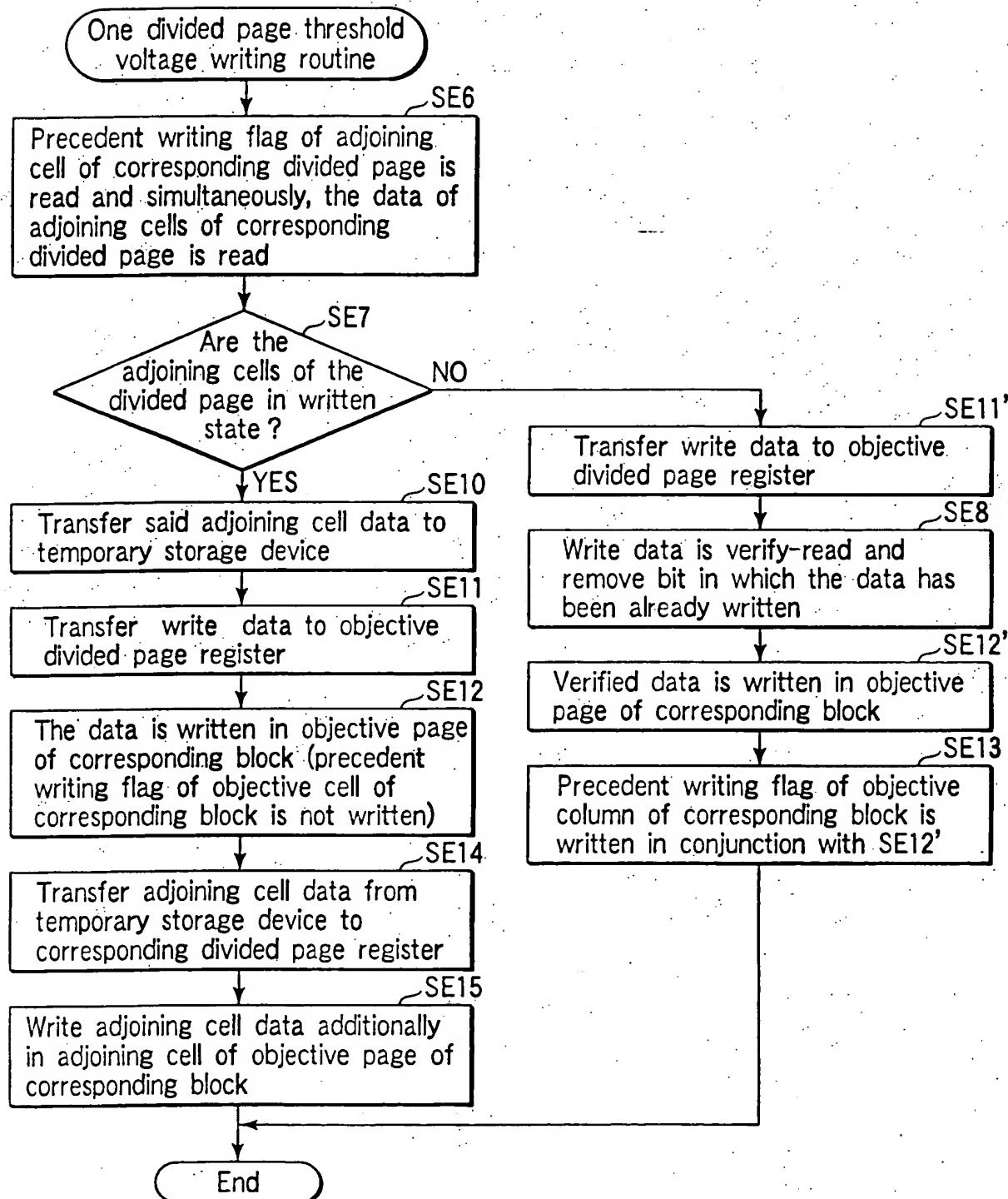


FIG. 3

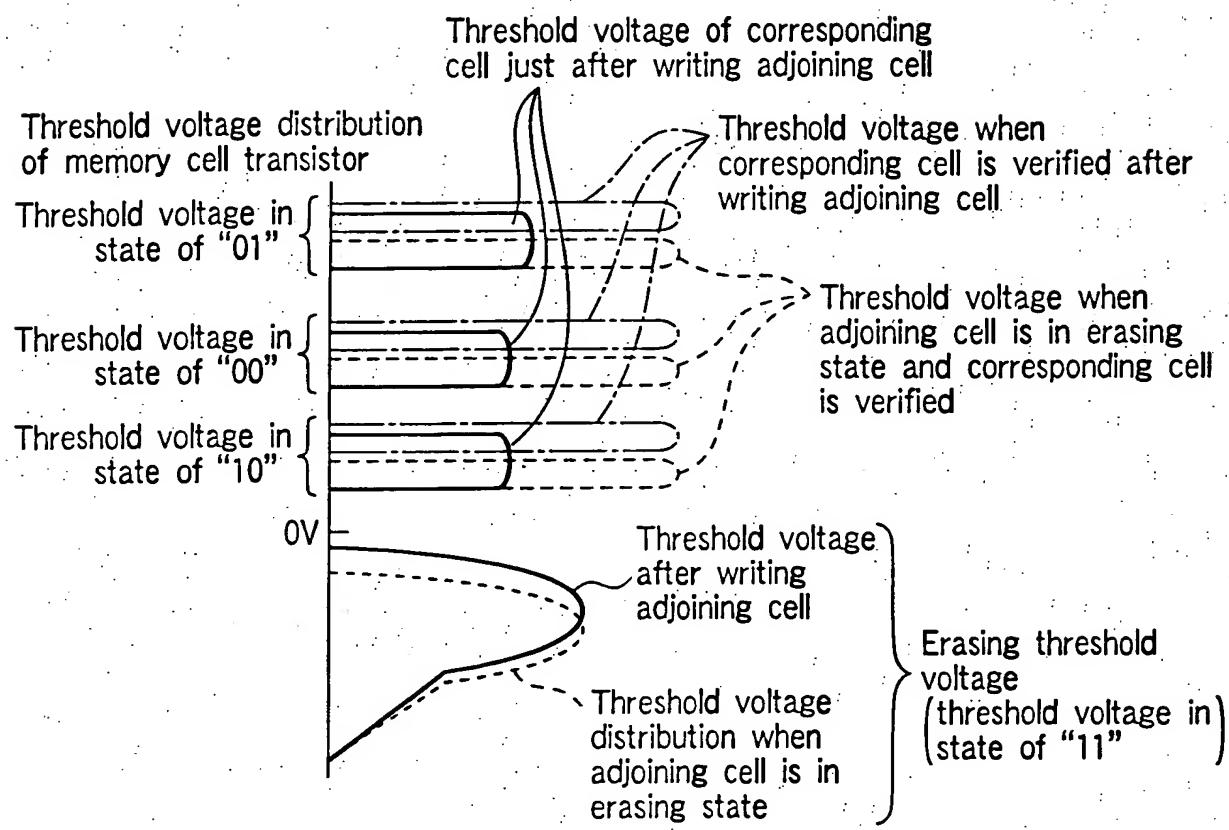


FIG. 4

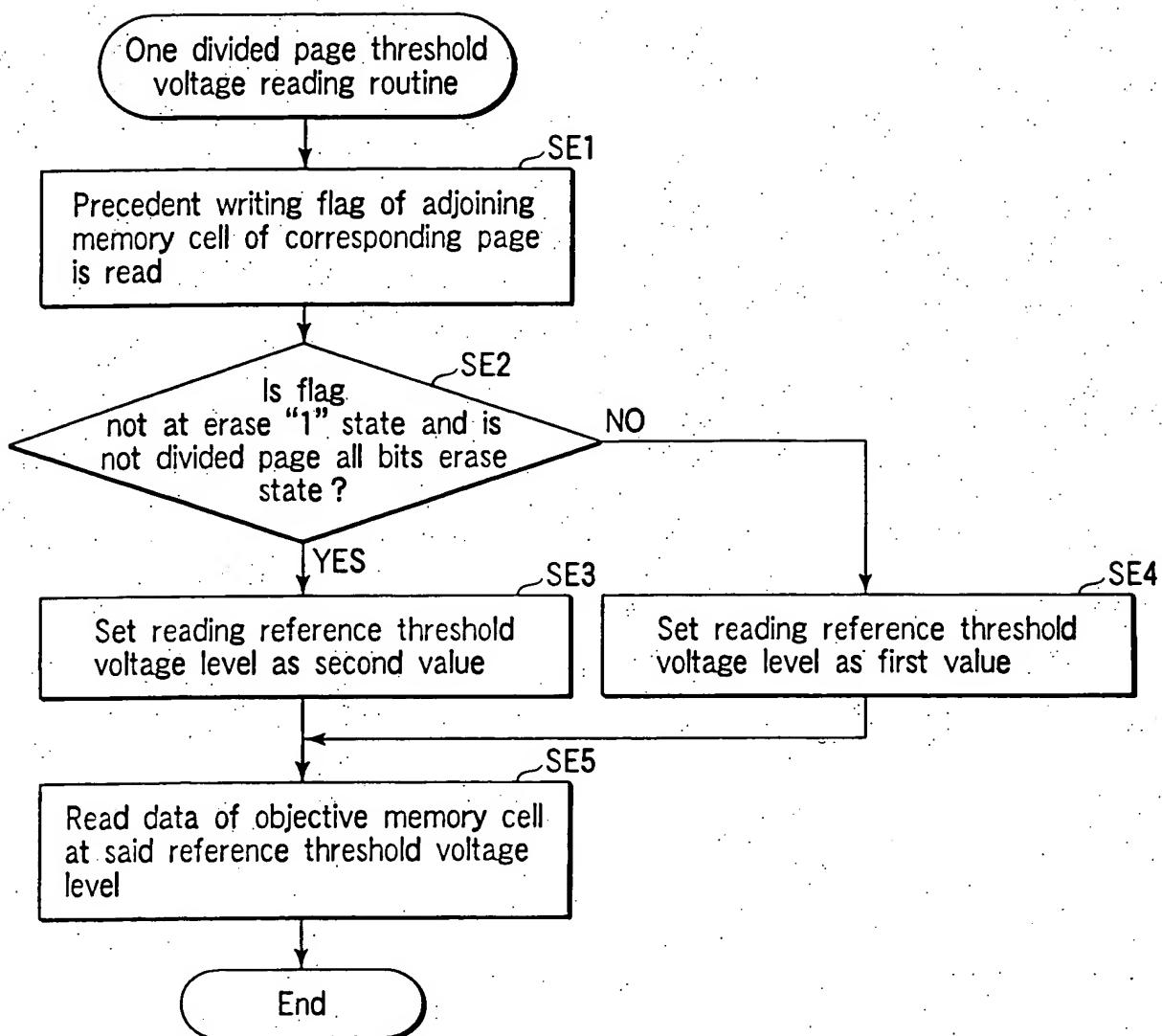


FIG. 5

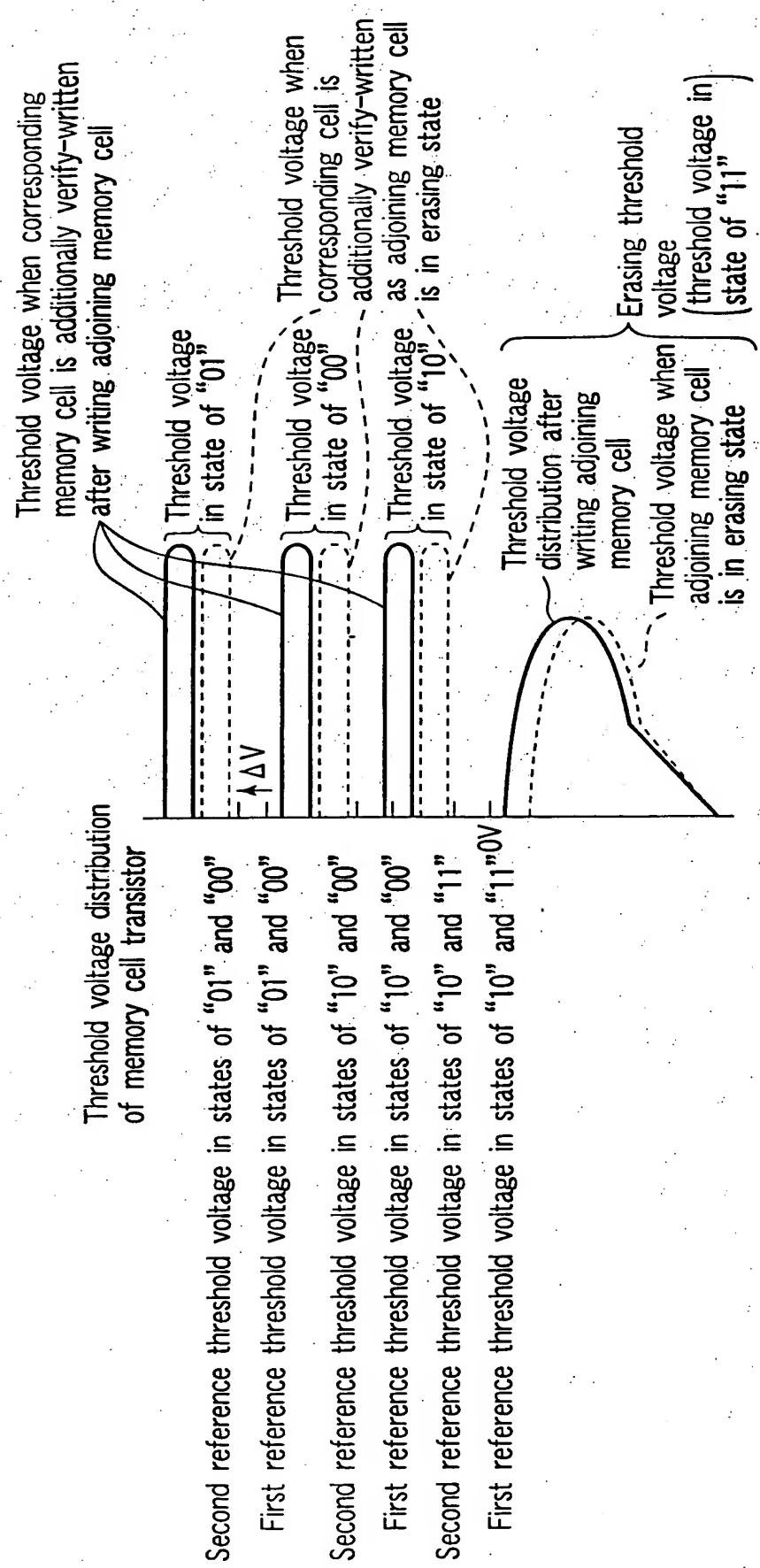
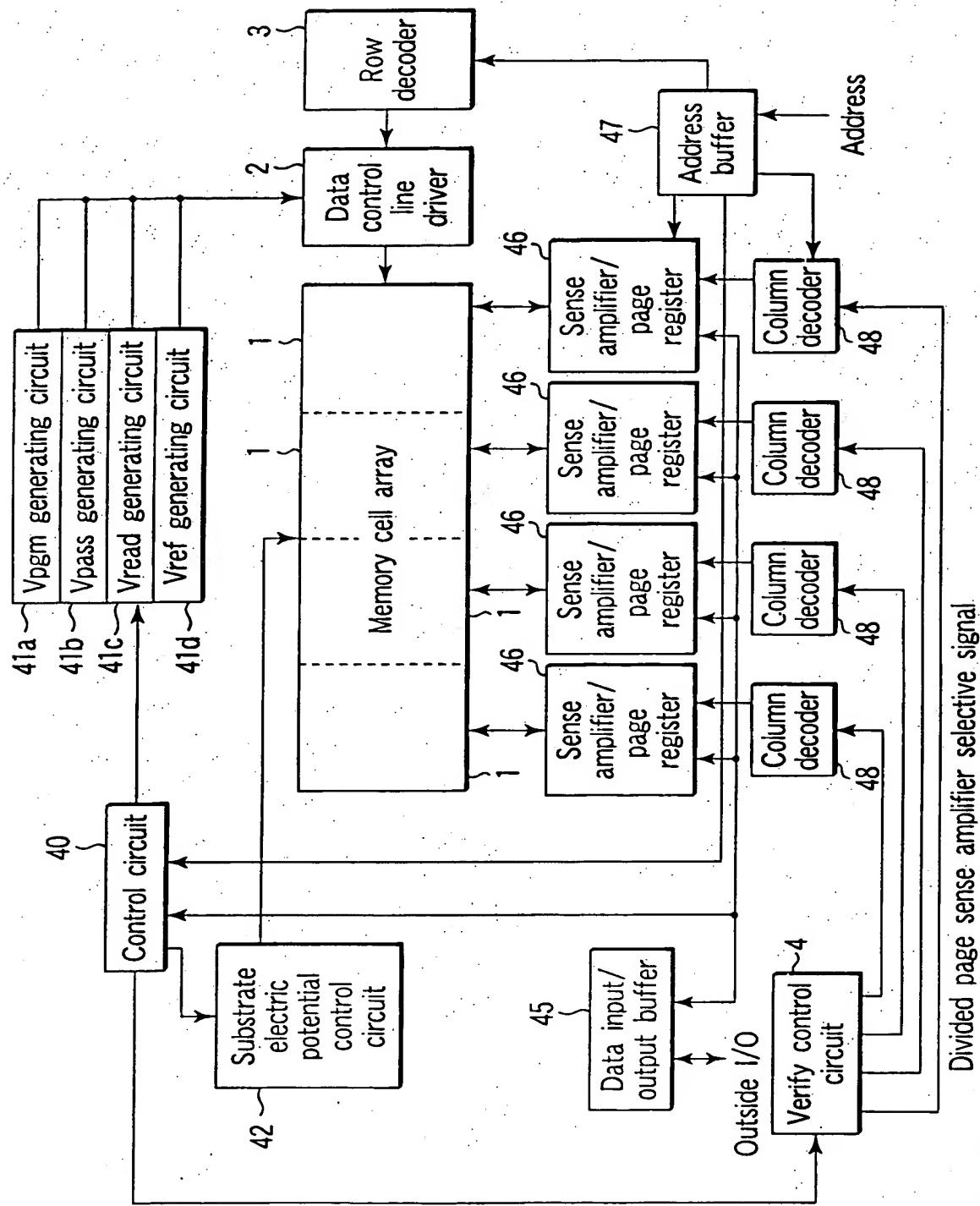


FIG. 6



Divided page sense amplifier selective signal

FIG. 7

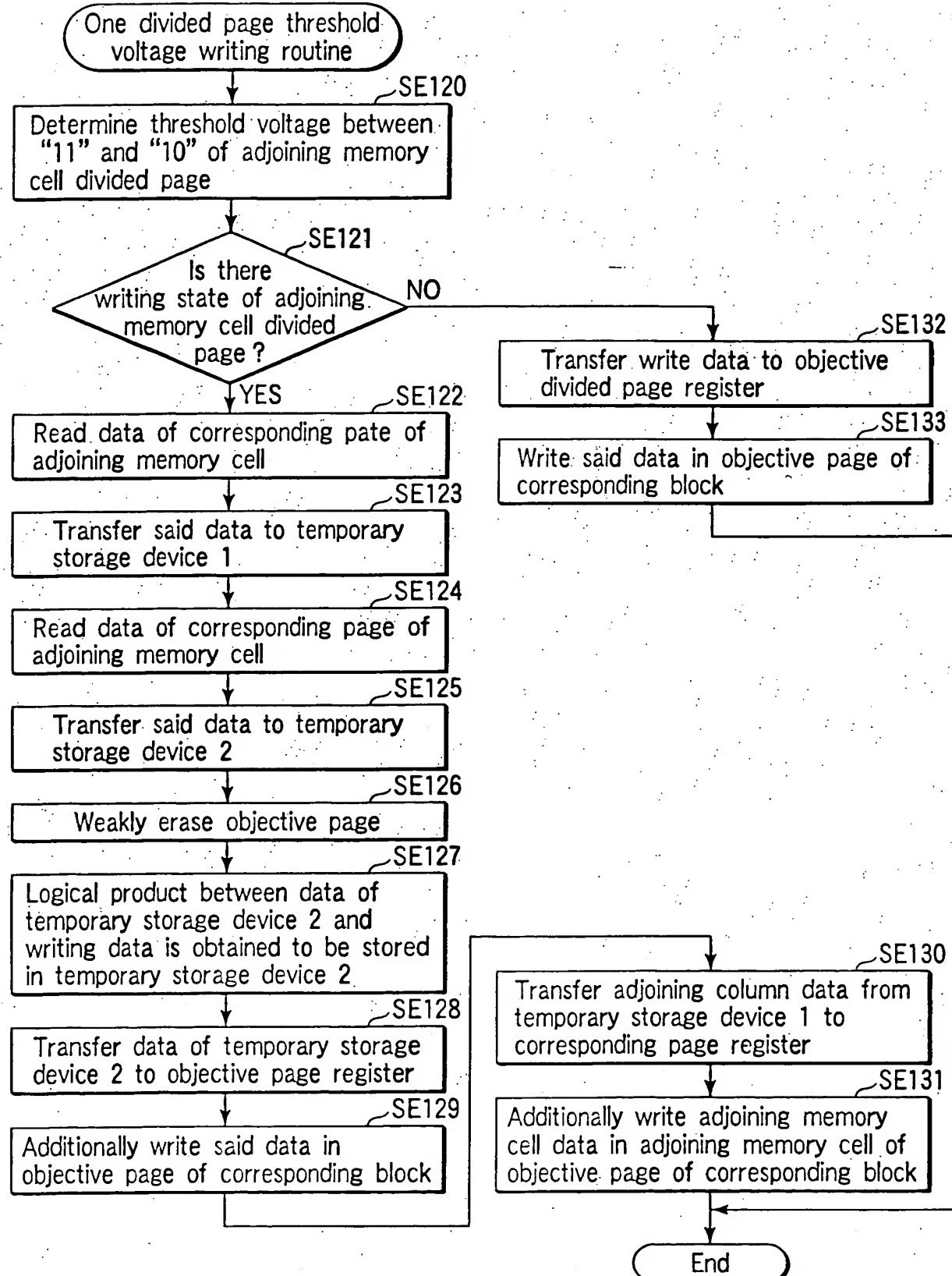


FIG. 8

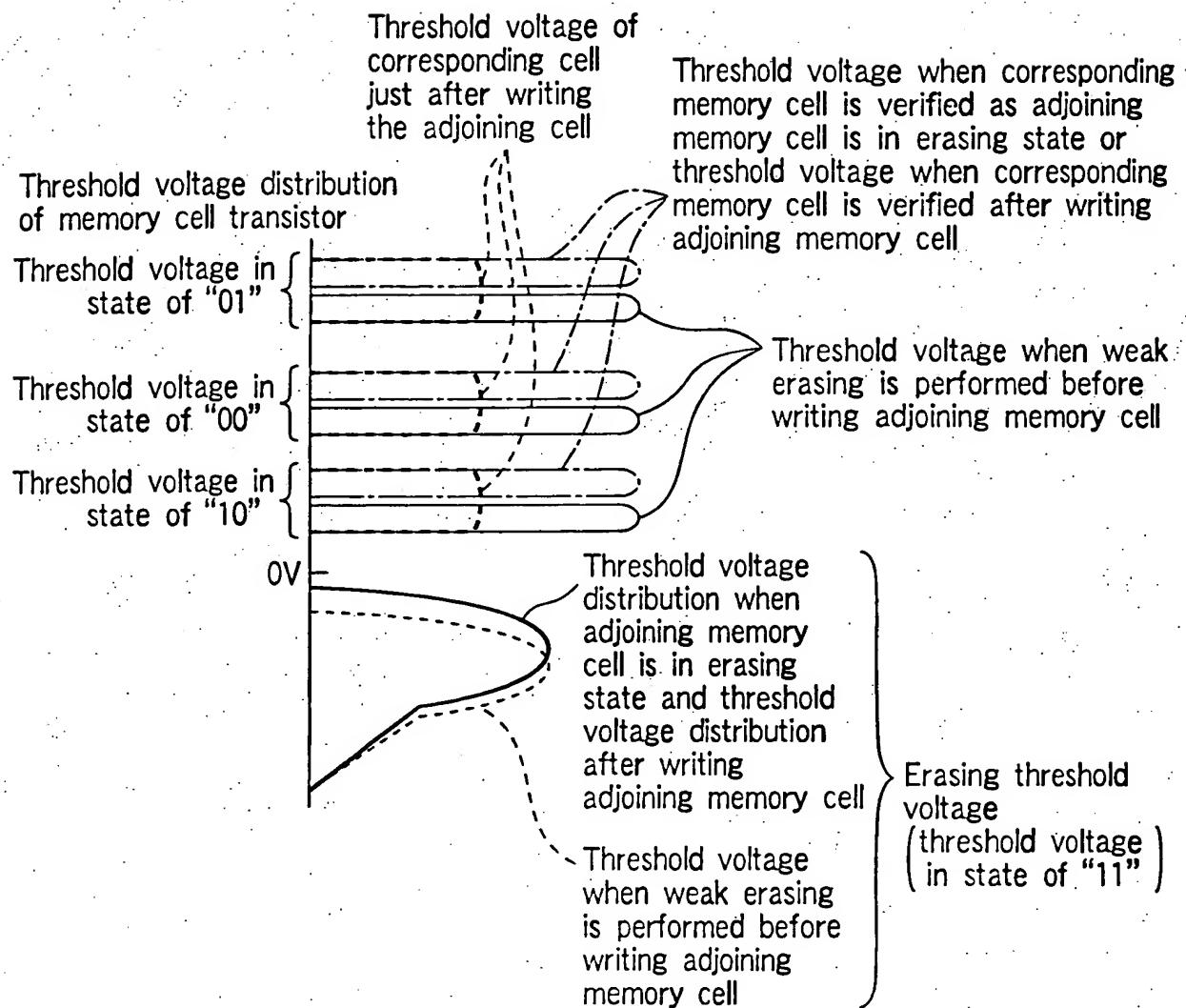


FIG. 9

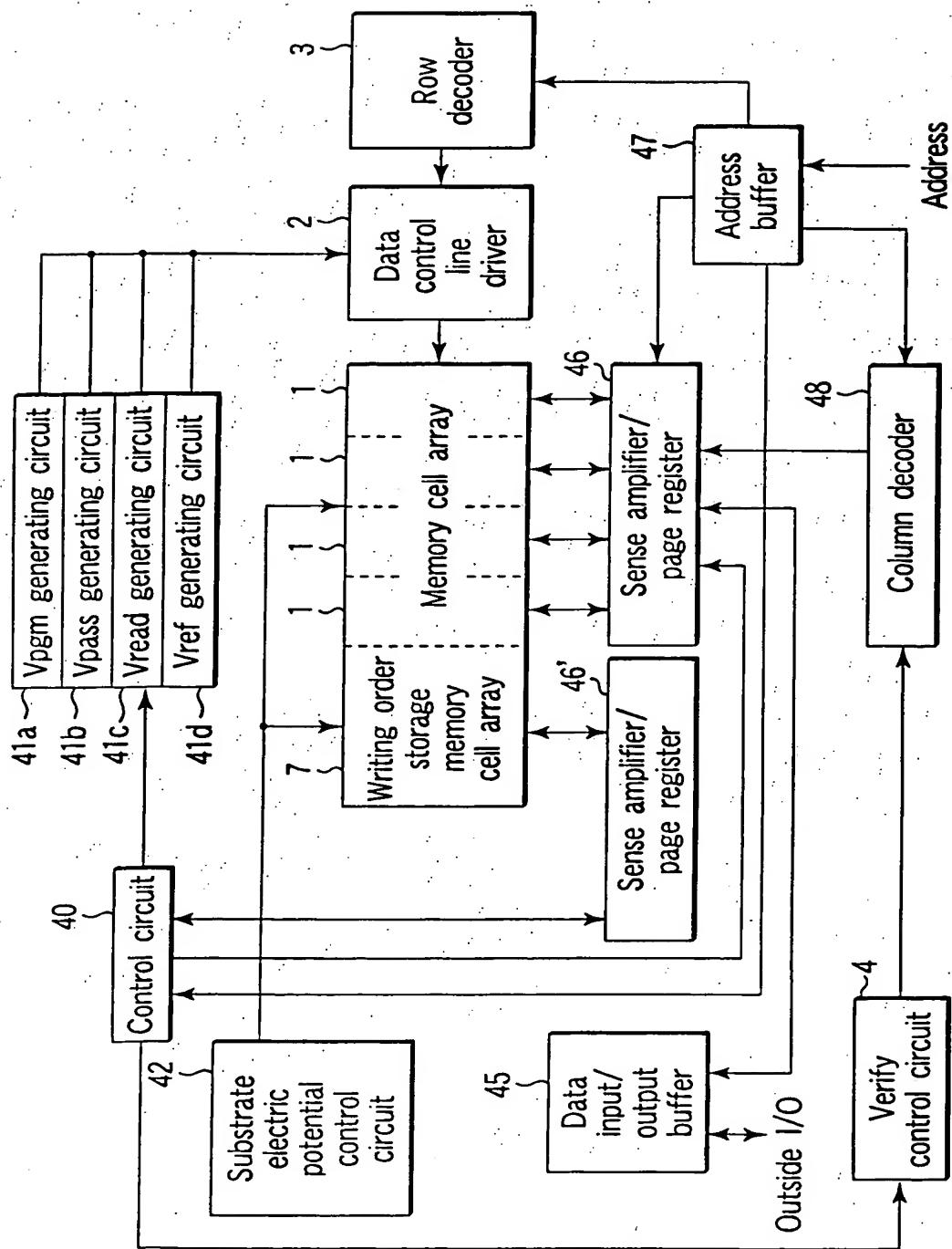
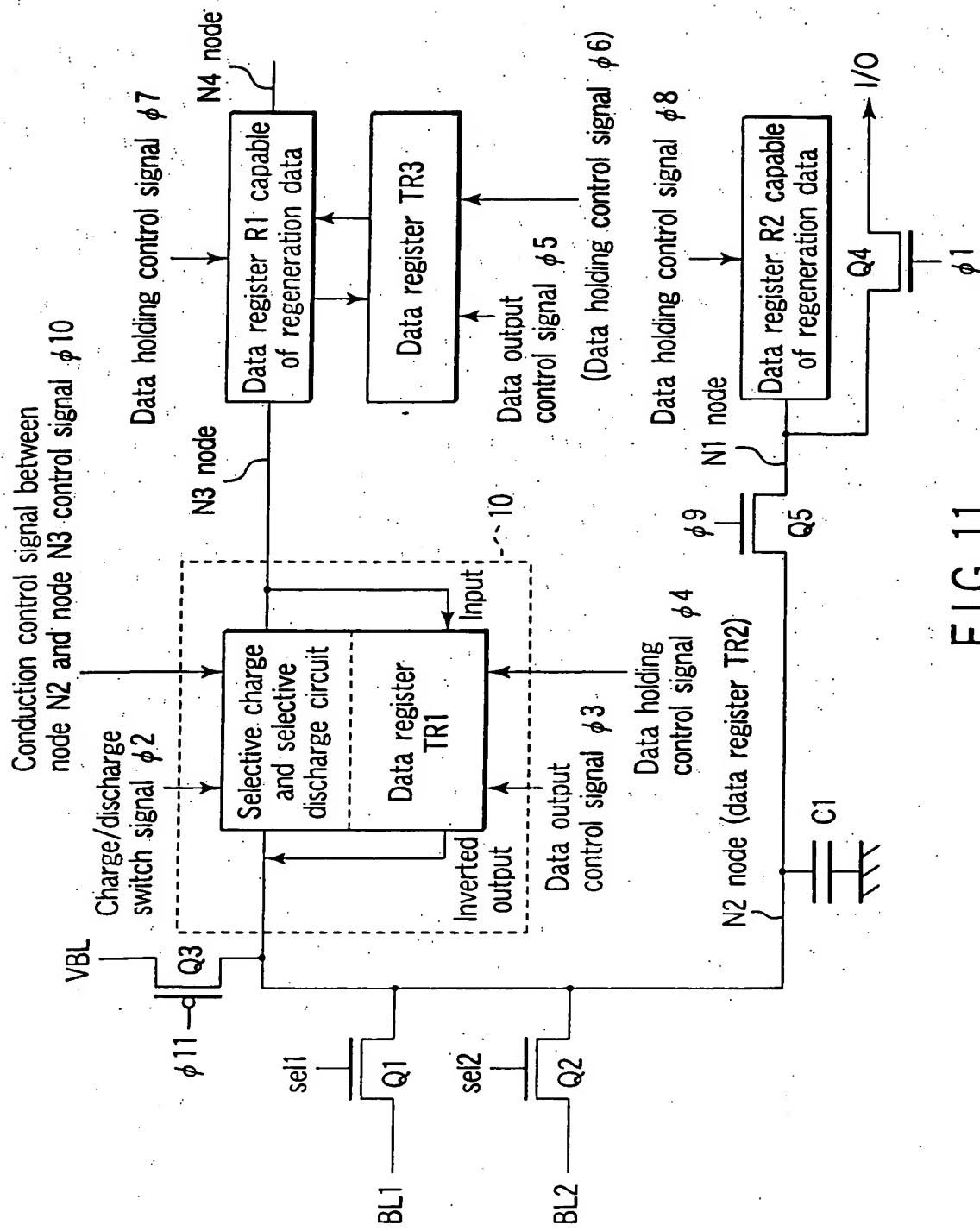
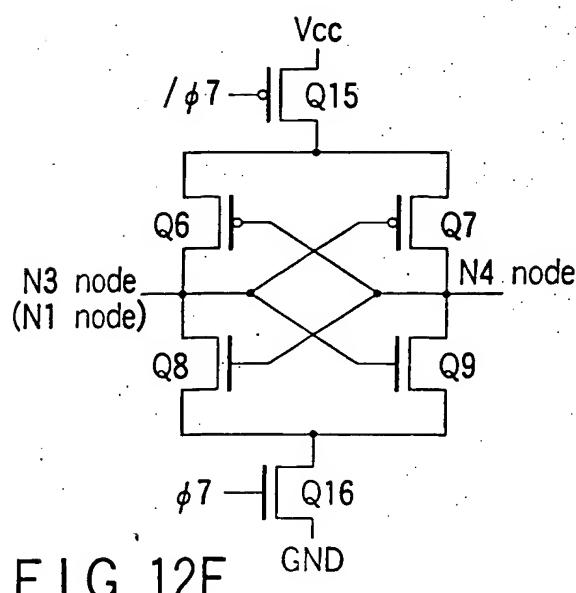
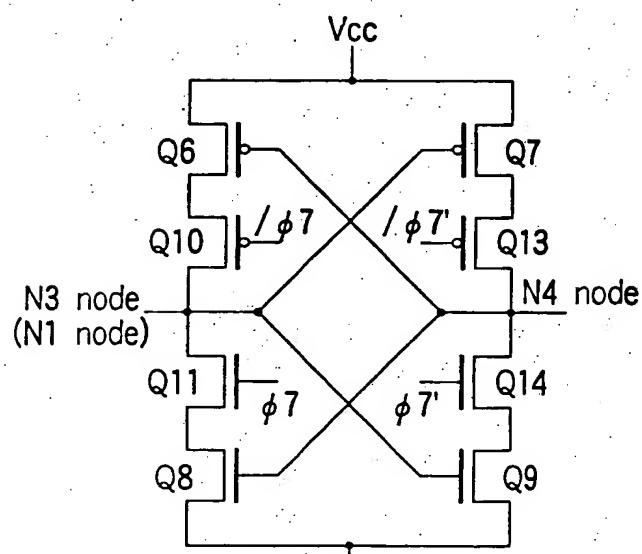
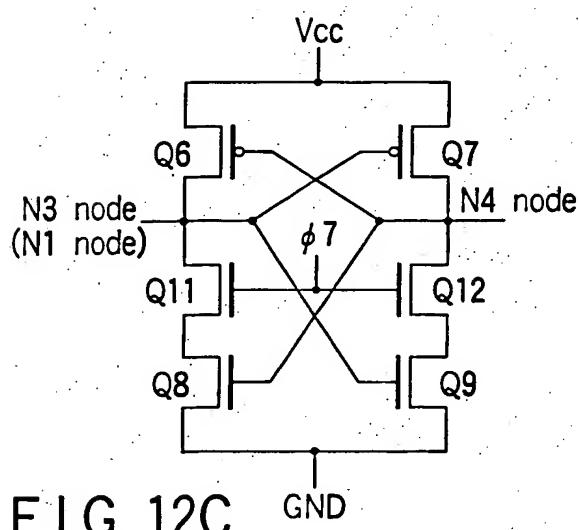
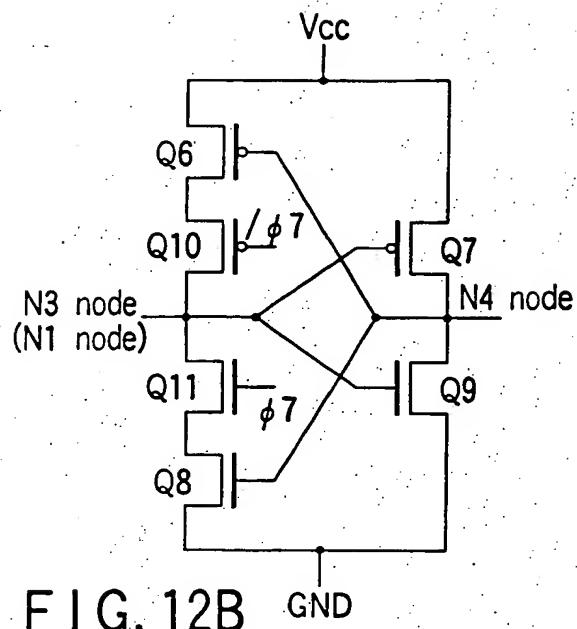
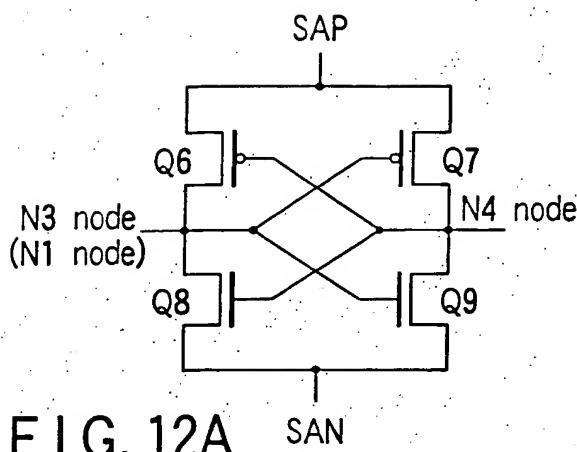


FIG. 10



三



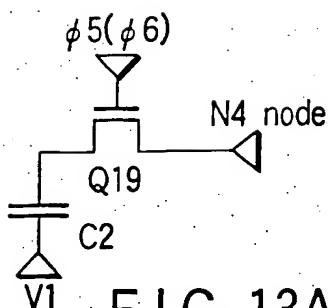


FIG. 13A

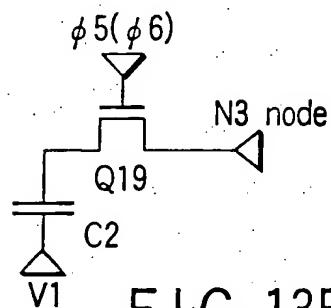


FIG. 13B

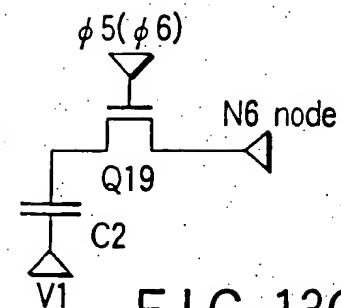


FIG. 13C

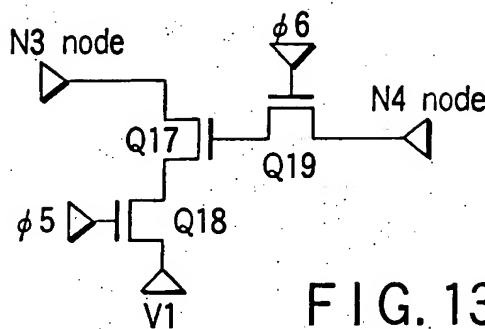


FIG. 13D

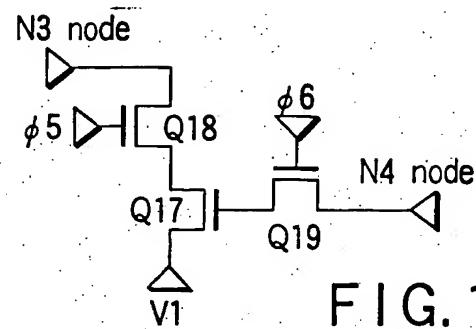


FIG. 13E

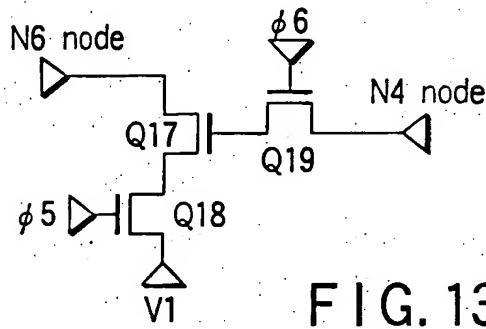


FIG. 13F

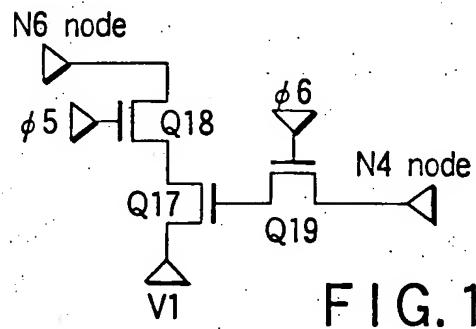


FIG. 13G

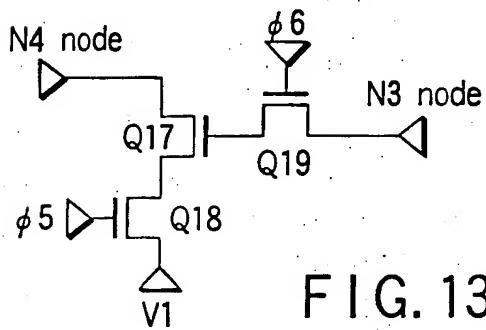


FIG. 13H

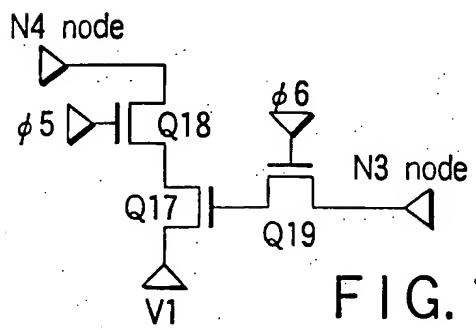


FIG. 131

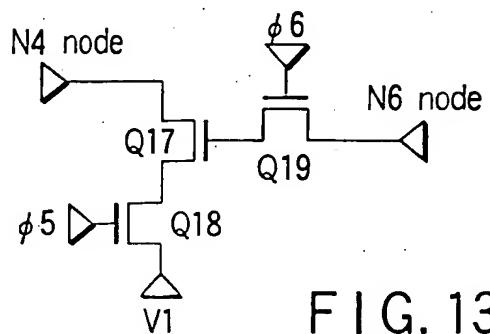


FIG. 13J

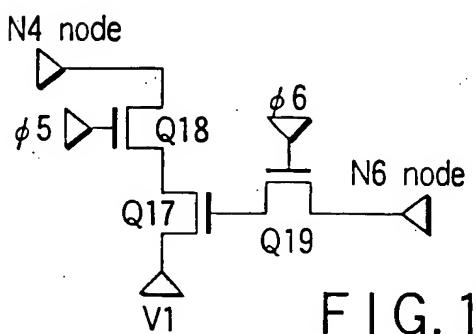


FIG. 13K

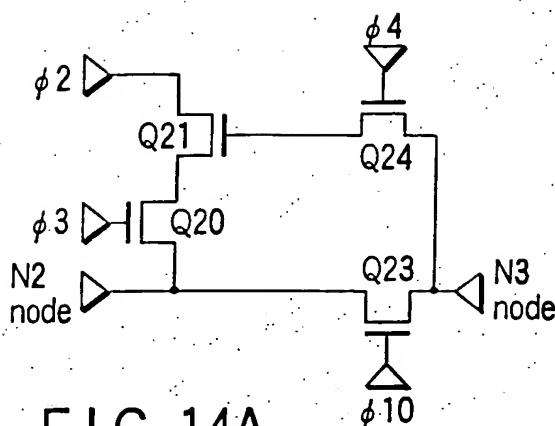


FIG. 14A

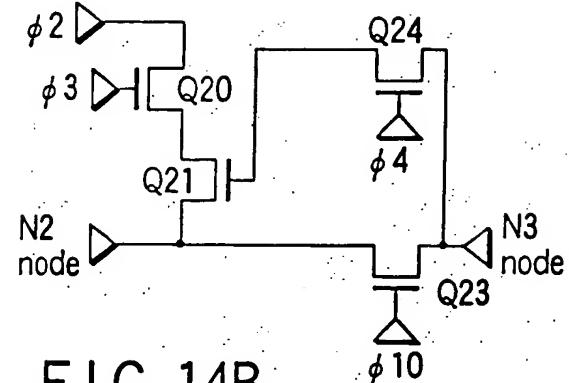


FIG. 14B

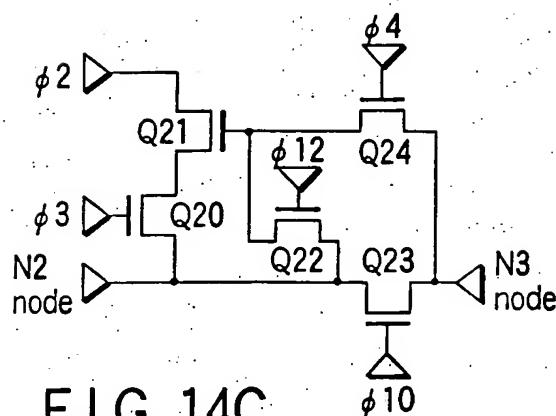


FIG. 14C

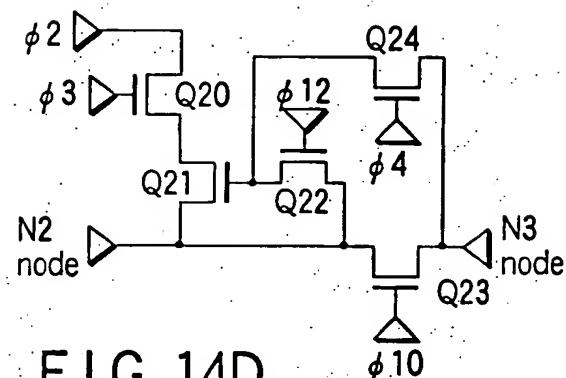


FIG. 14D

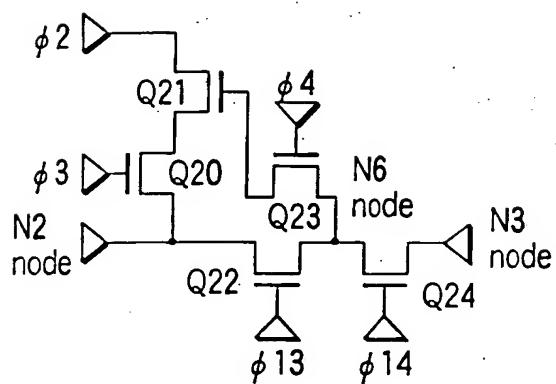


FIG. 14E

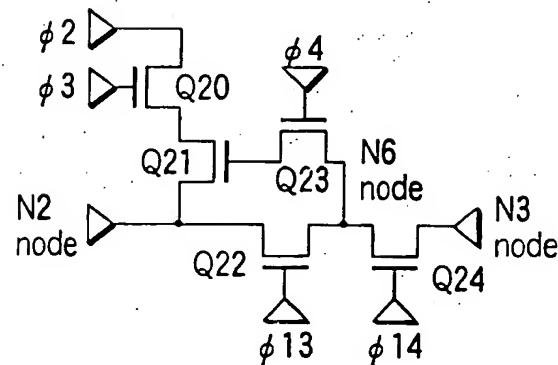


FIG. 14F

Q21 gate initial value (corresponding to TR1)	N2 node initial value	N2 node output after $\phi 3 = "H"$
"L"	"L"	"L"
"H"	"L"	"L"
"L"	"H"	"H"
"H"	"H"	"L"

In the case of  $\phi 2 = "L"$   
(selective discharge operation and inverted output operation)

FIG. 15A

Q21 gate initial value (corresponding to TR1)	N2 node initial value	N2 node output after $\phi 3 = "H"$
"L"	"L"	"L"
"H"	"L"	"H"
"L"	"H"	"H"
"H"	"H"	"H"

In the case of  $\phi 2 = "H"$   
(selective charge operation)

FIG. 15B

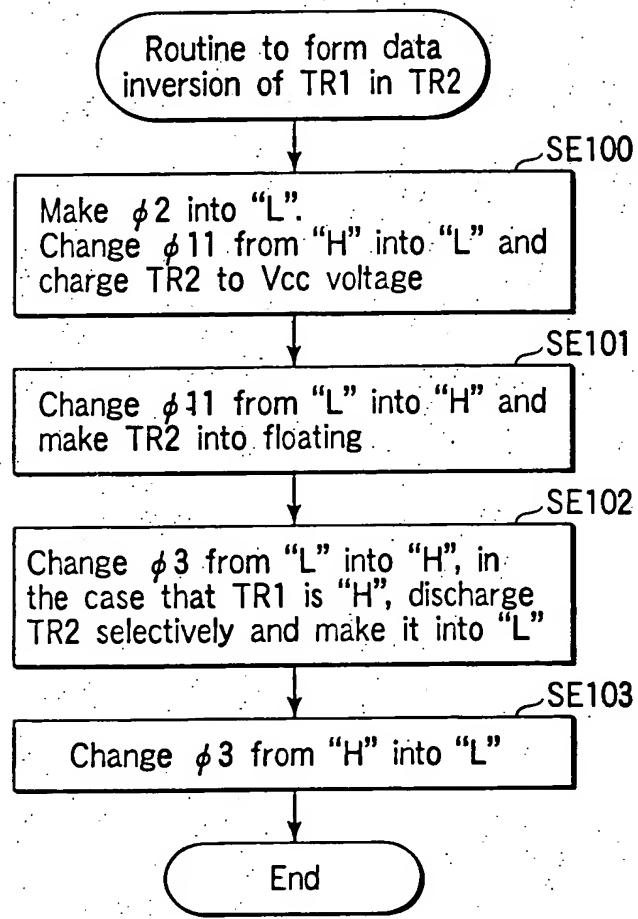


FIG. 16

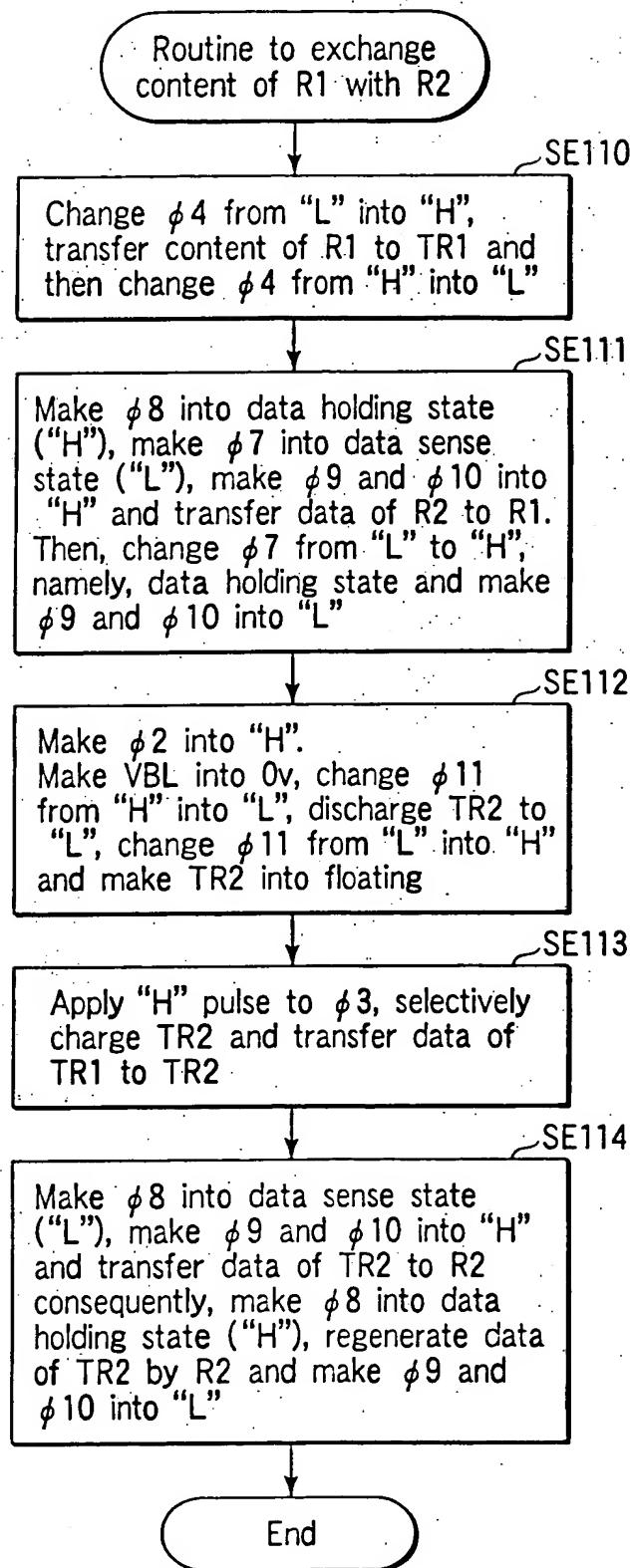


FIG. 17

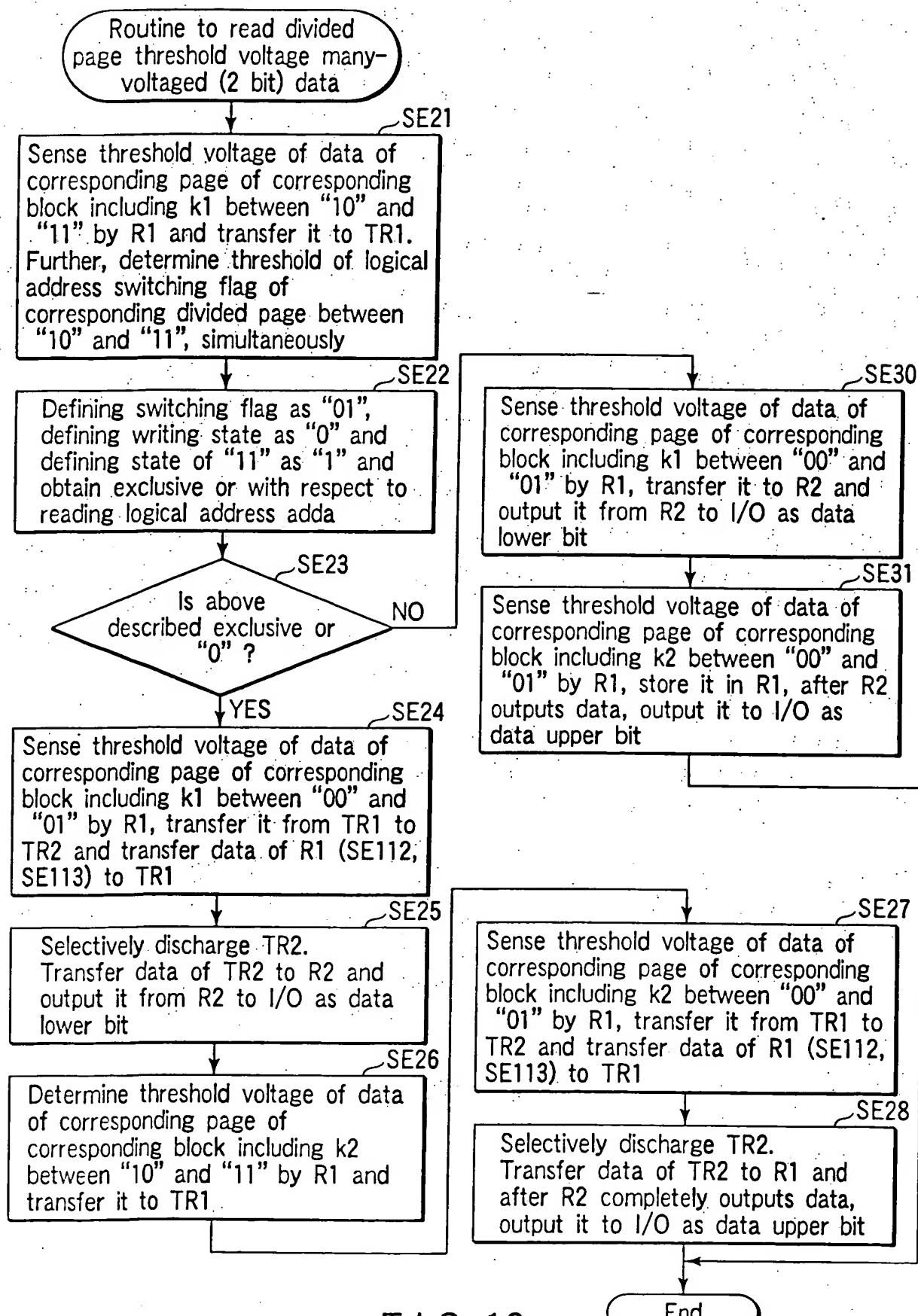


FIG. 18

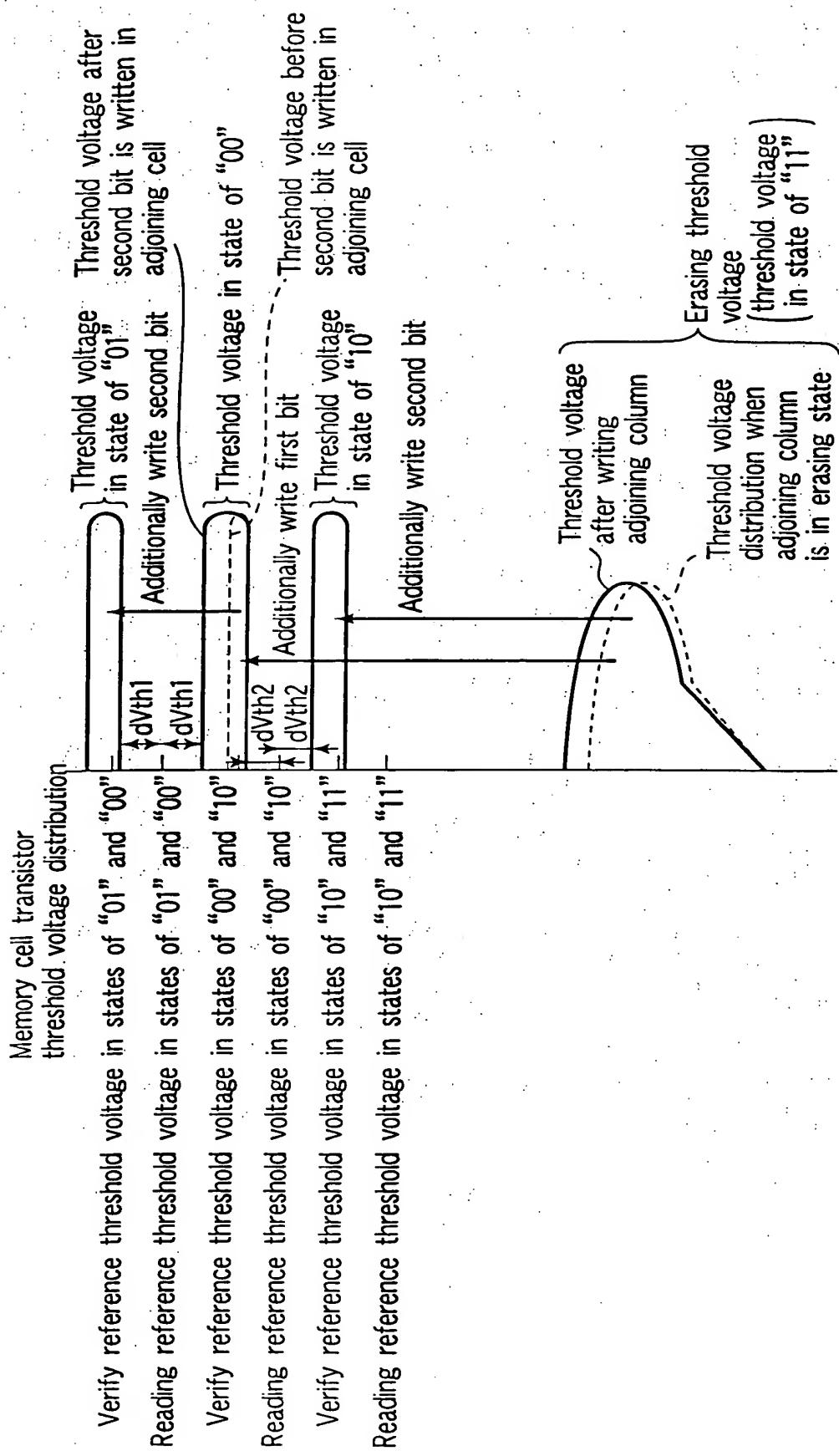


FIG. 19

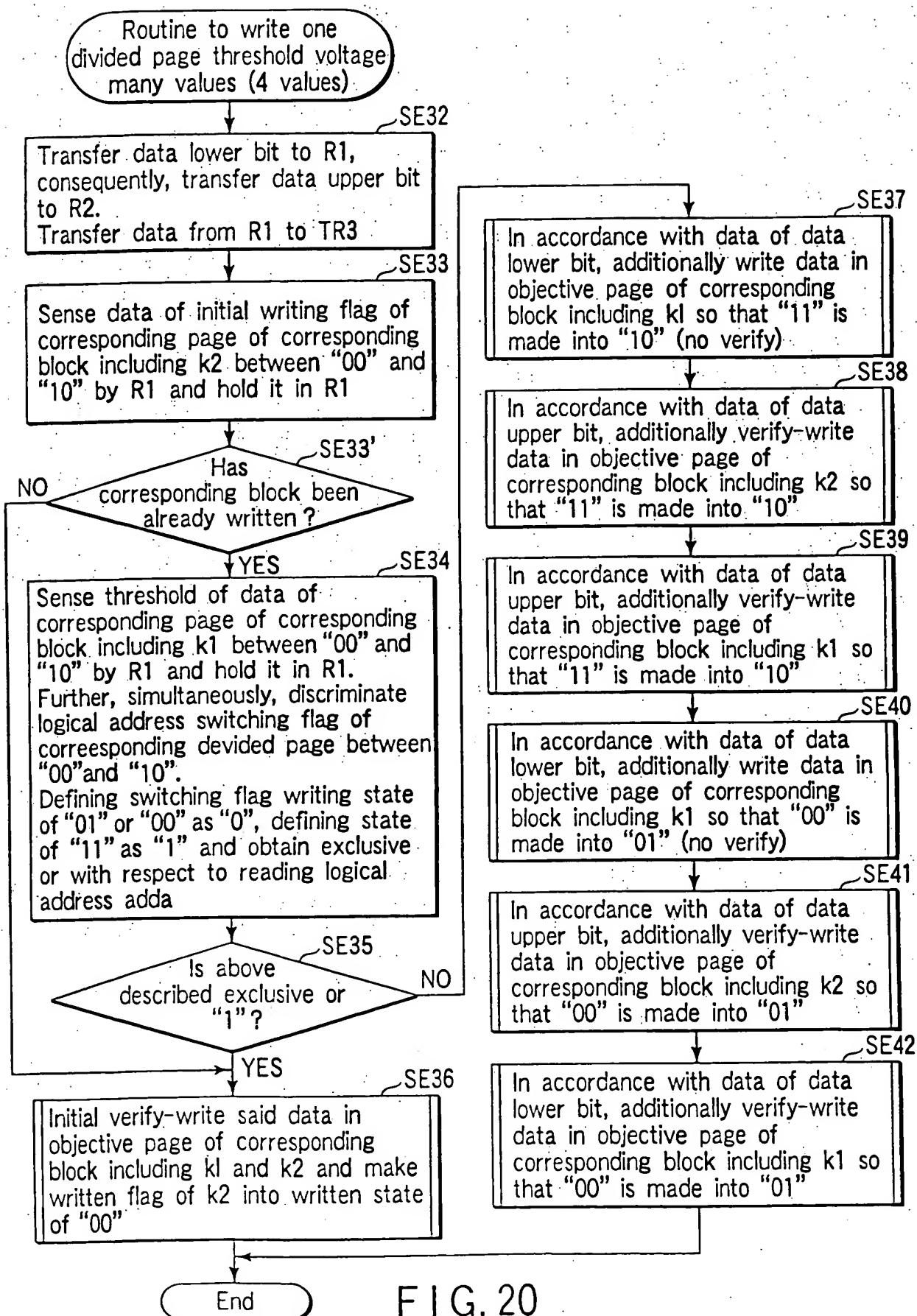


FIG. 20

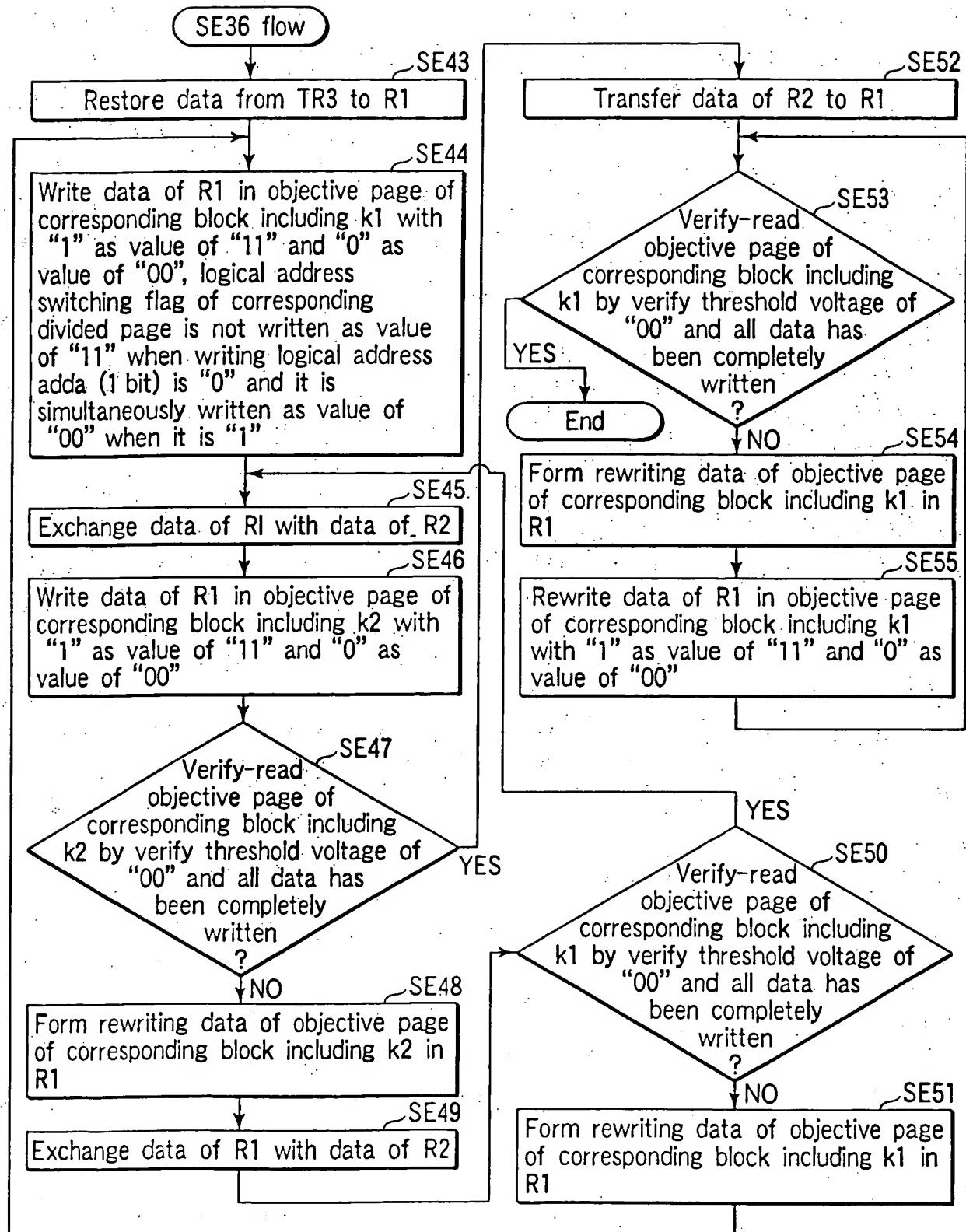


FIG. 21

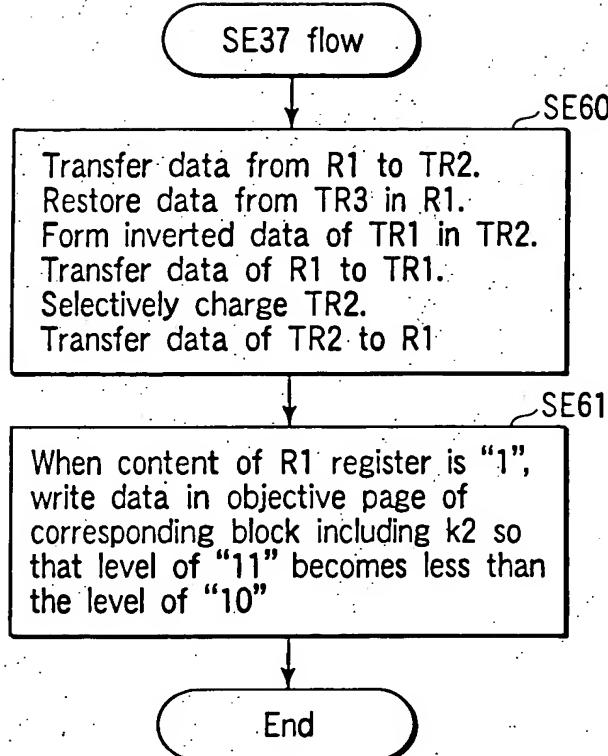


FIG. 22

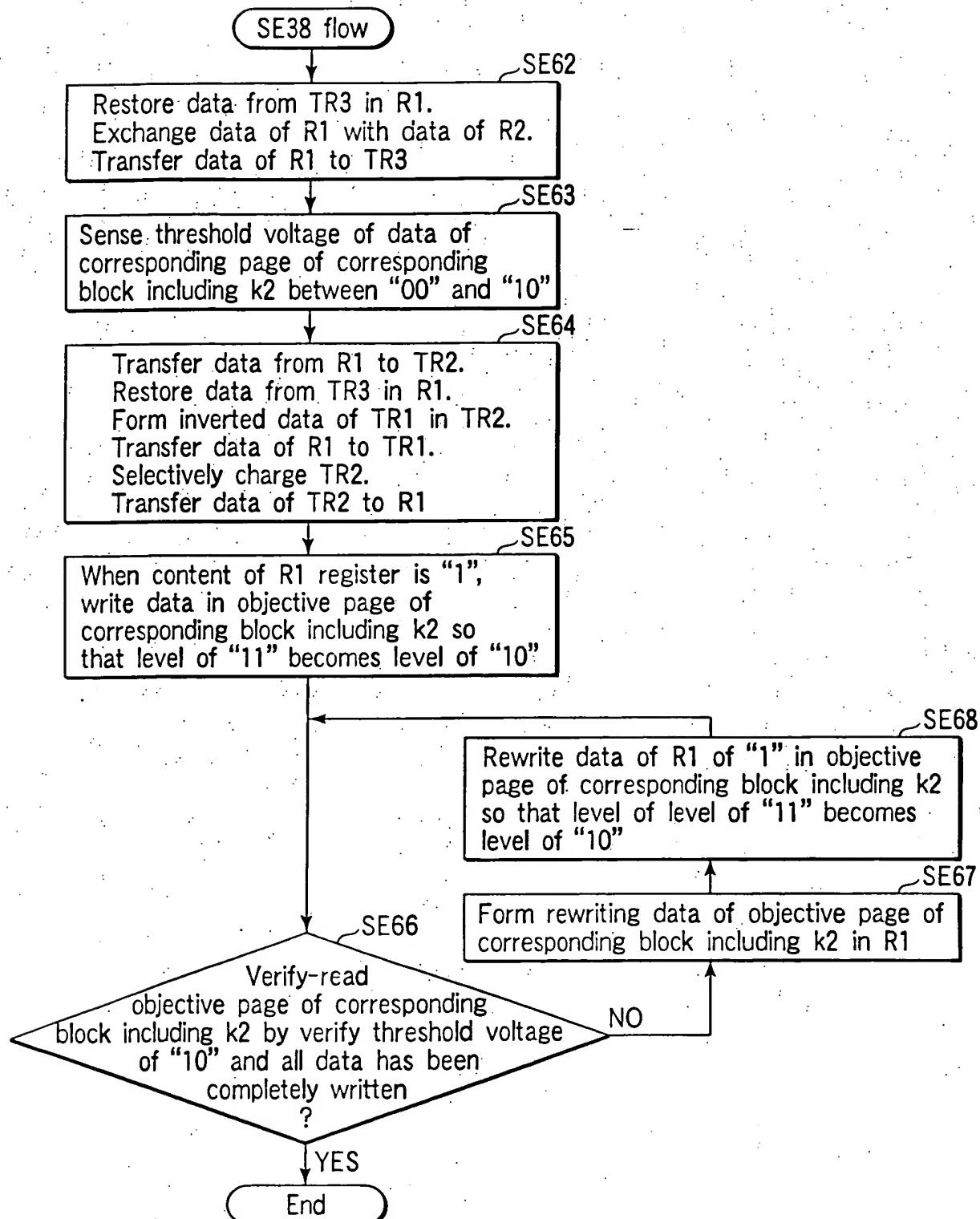


FIG. 23

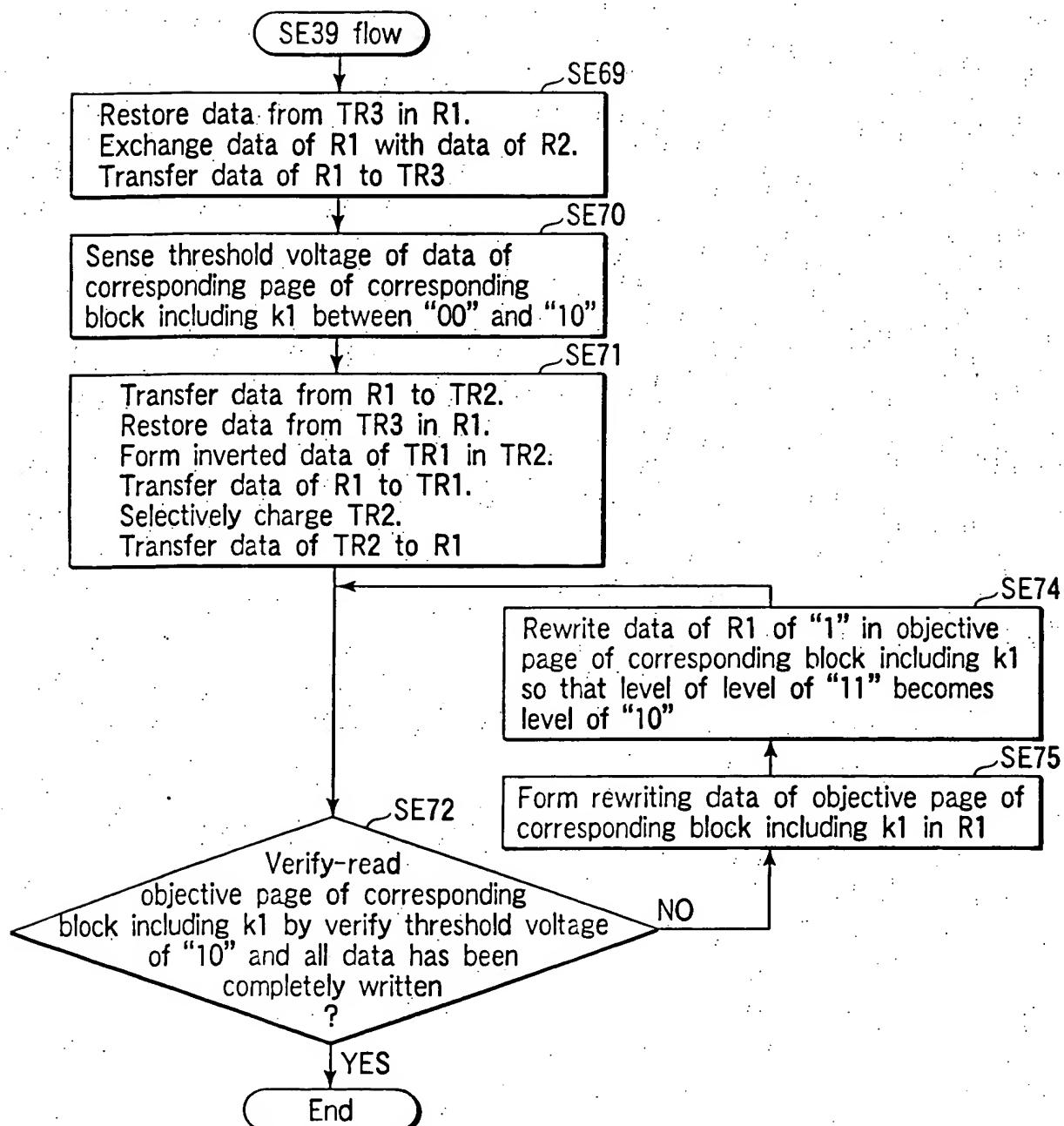


FIG. 24

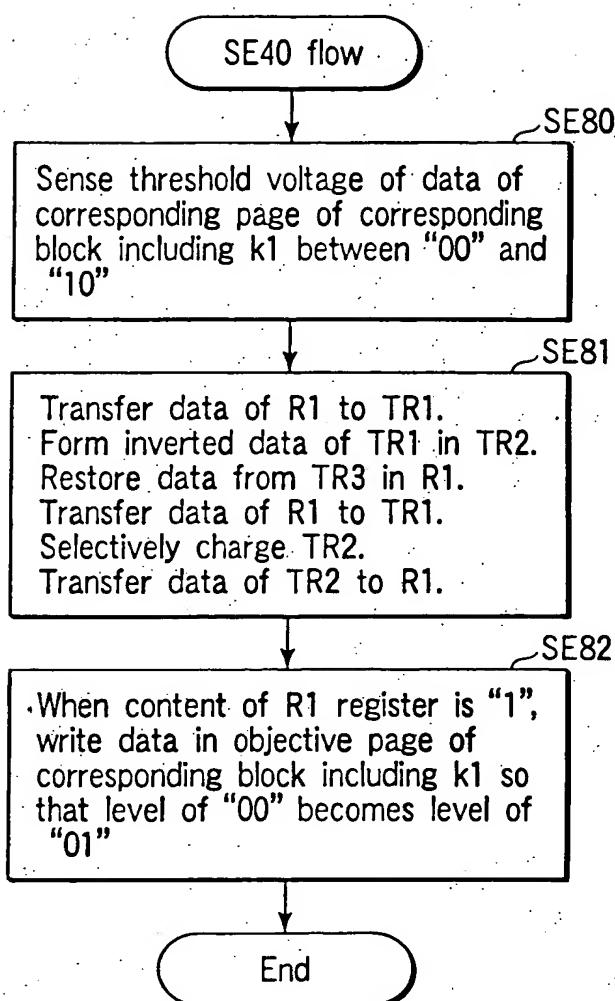


FIG. 25

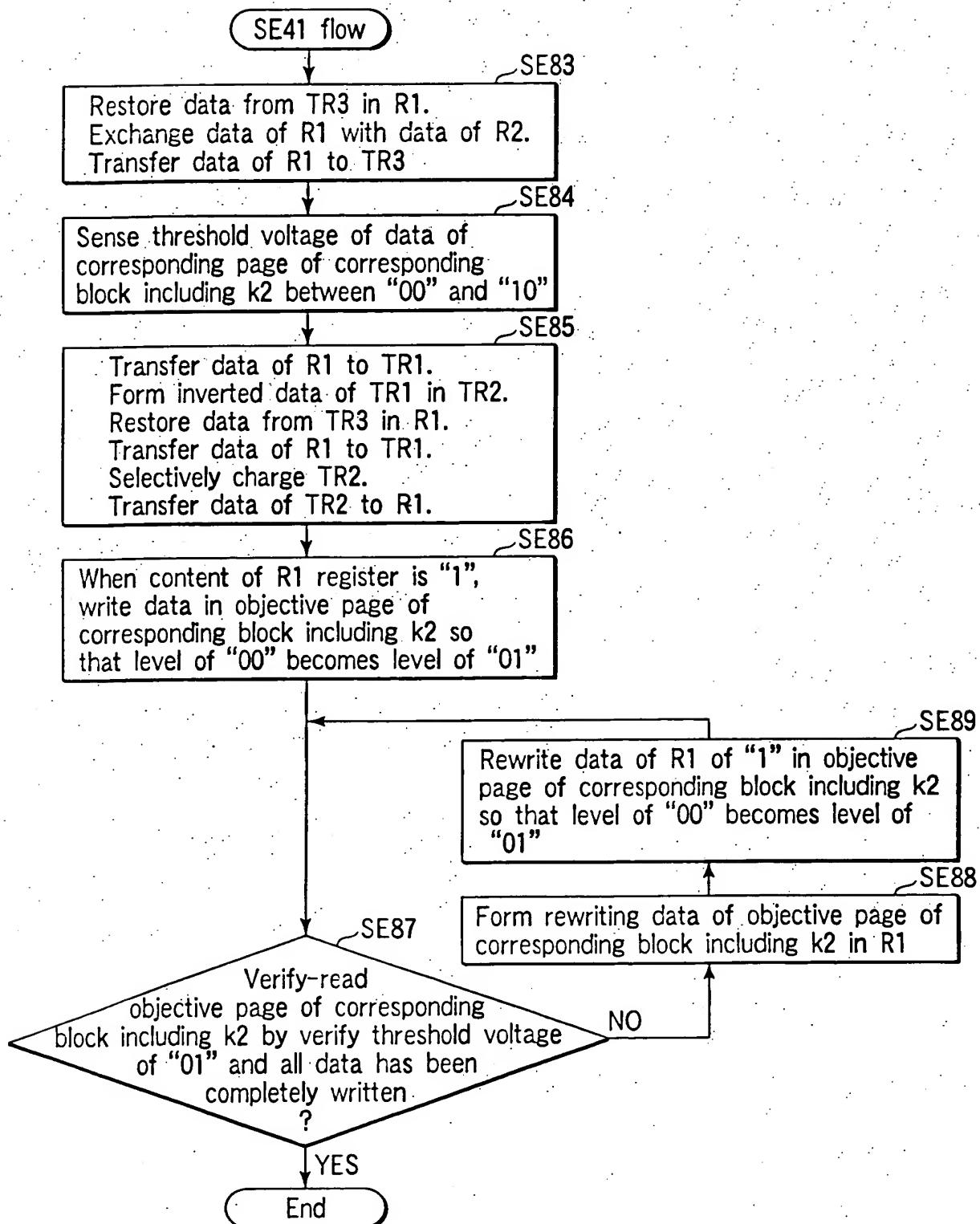


FIG. 26

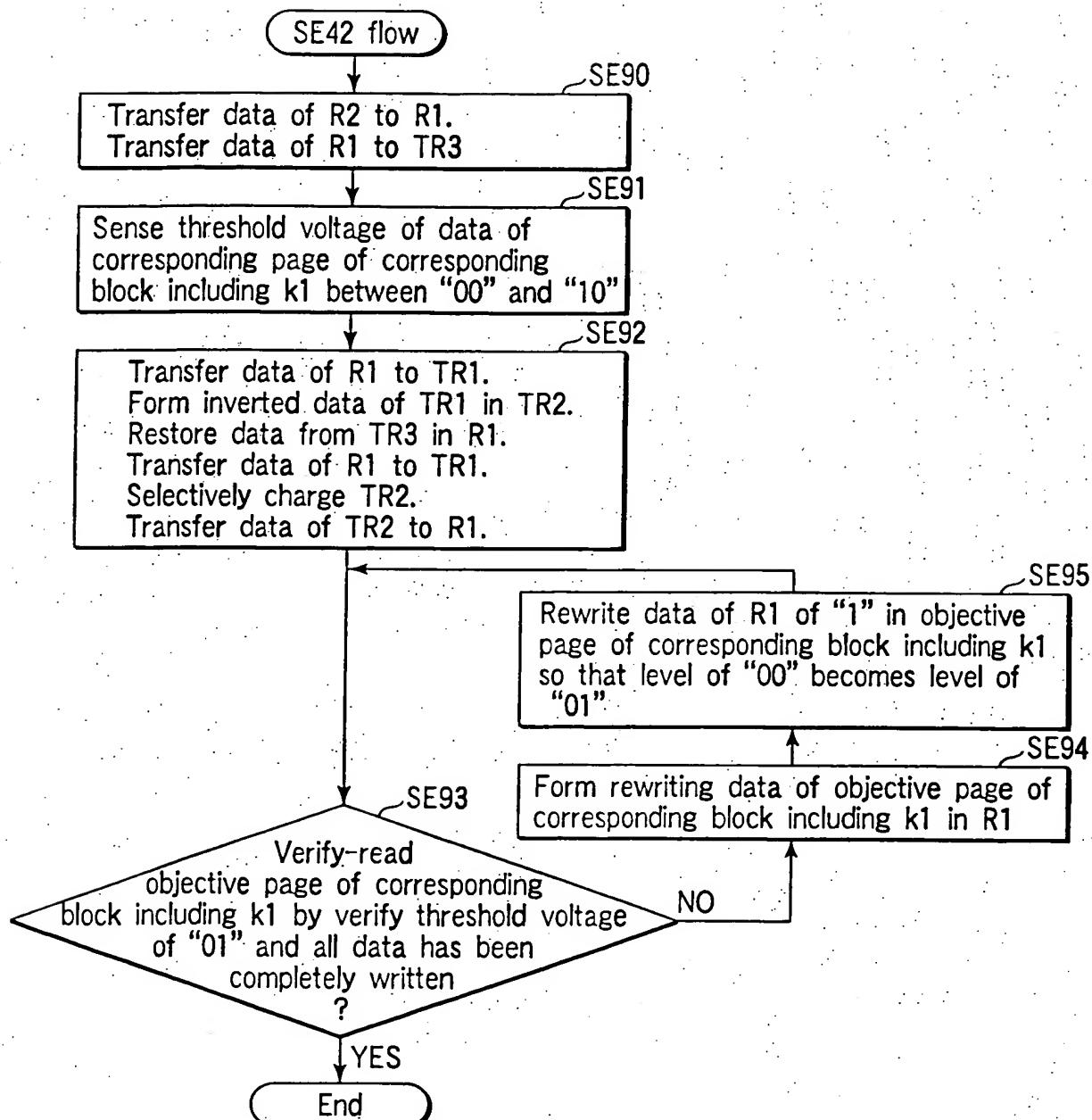


FIG. 27

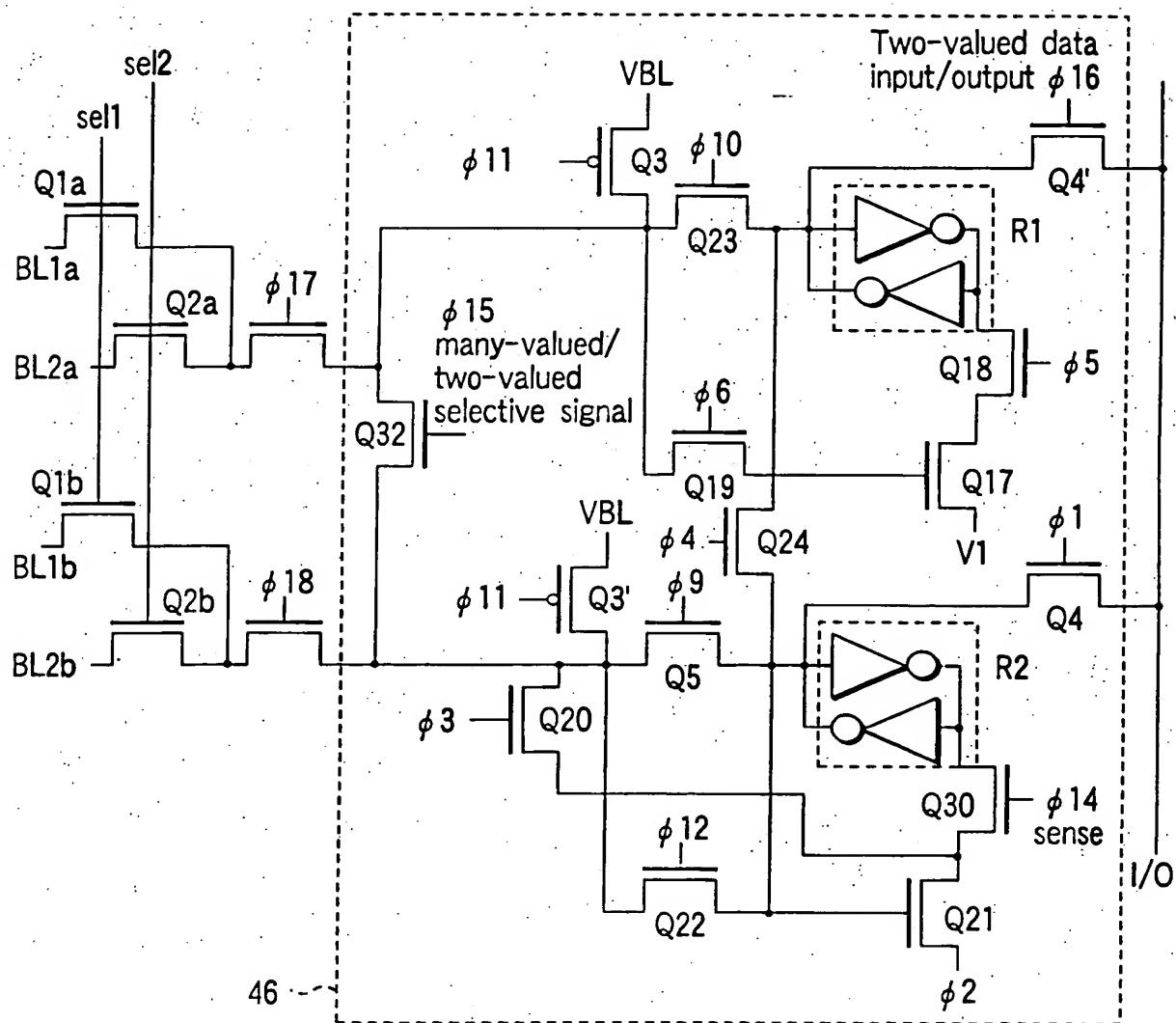


FIG. 28

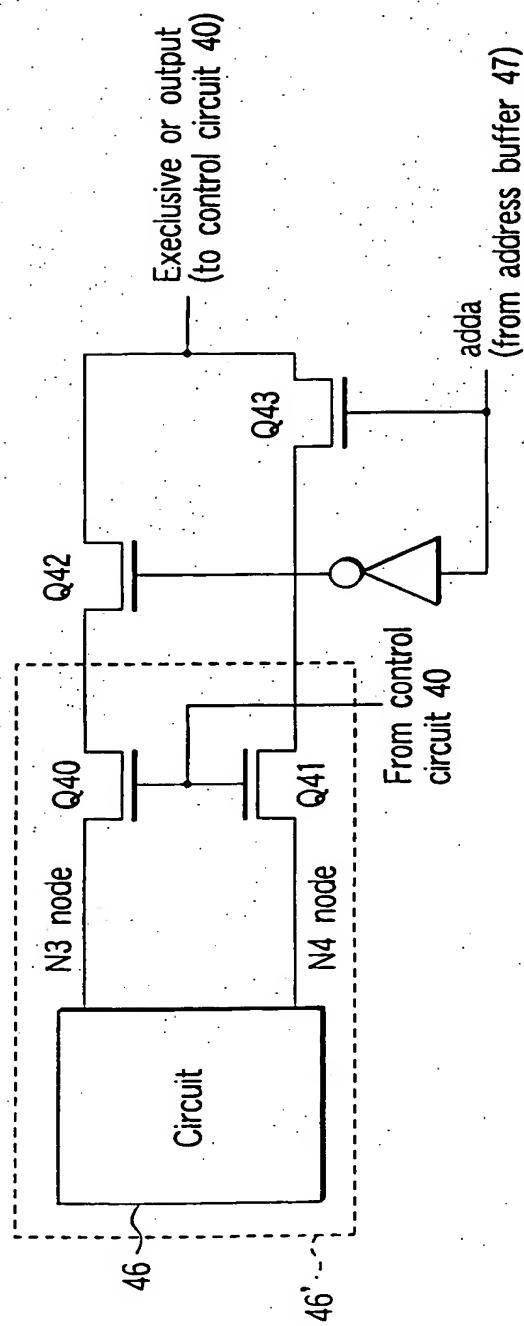


FIG. 29

Memory cell transistor threshold voltage distribution

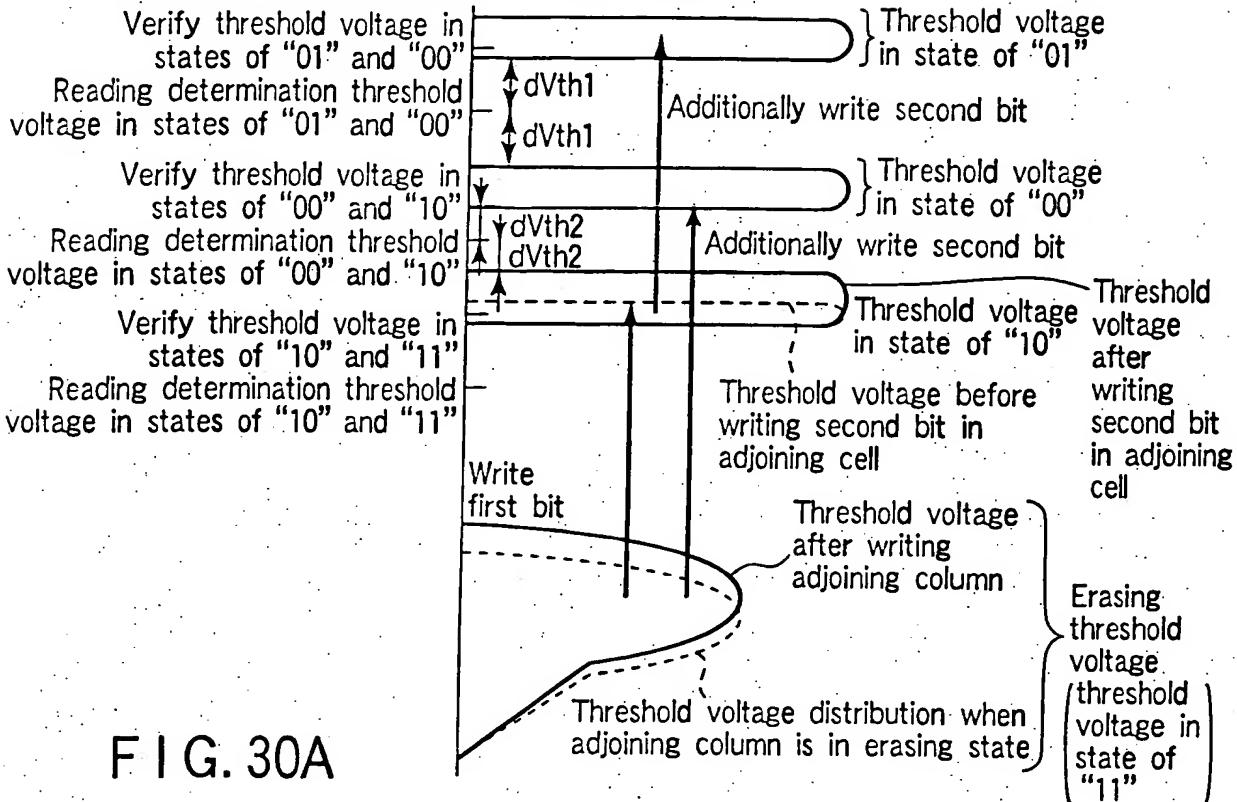


FIG. 30A

Memory cell transistor threshold voltage distribution

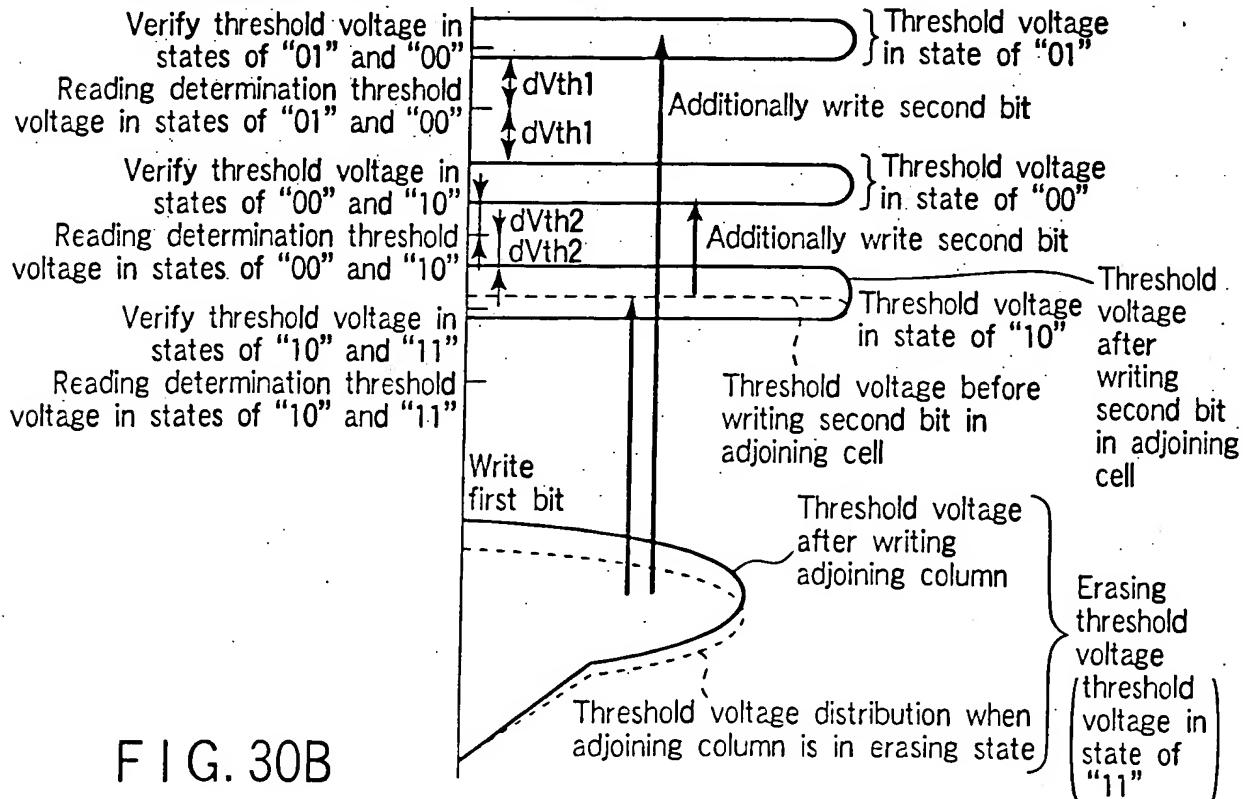


FIG. 30B

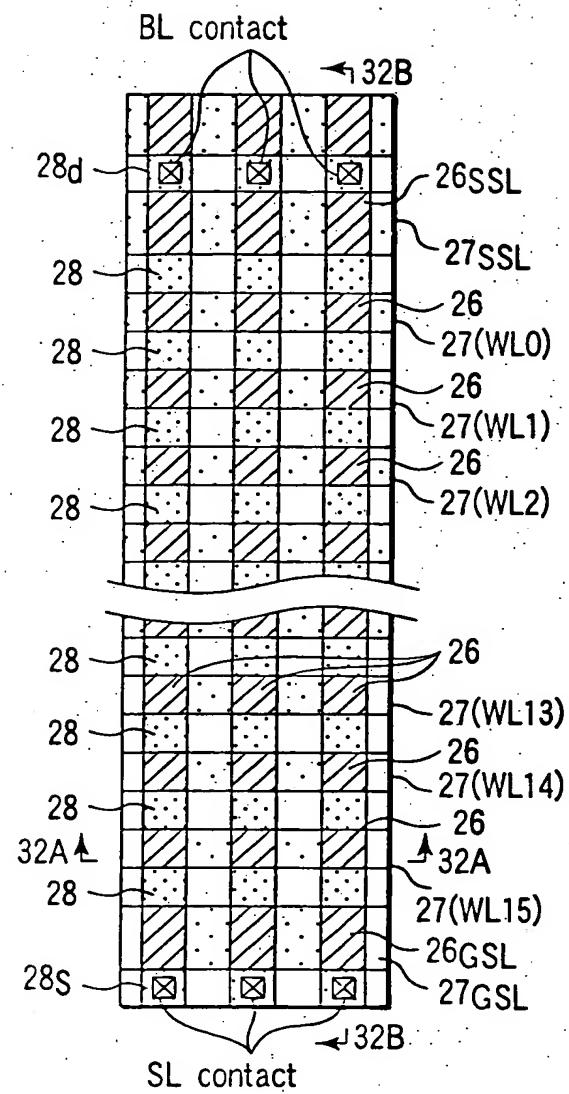
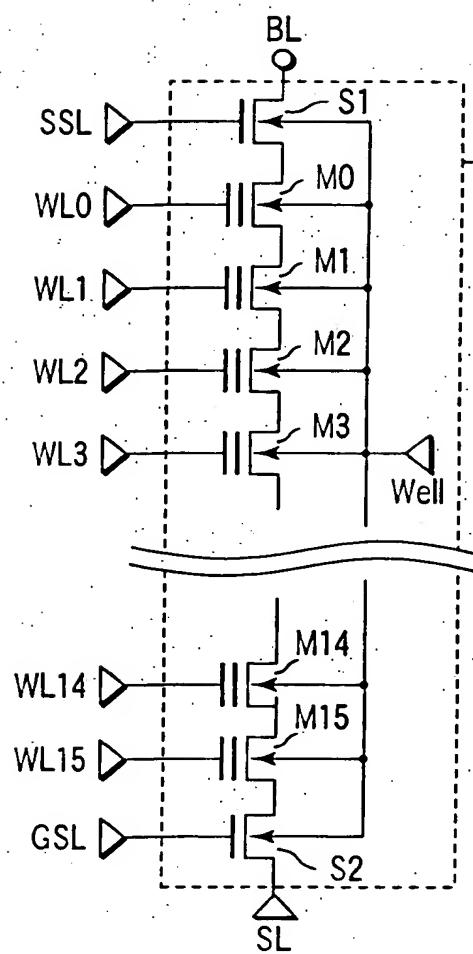


FIG. 31A

FIG. 31B

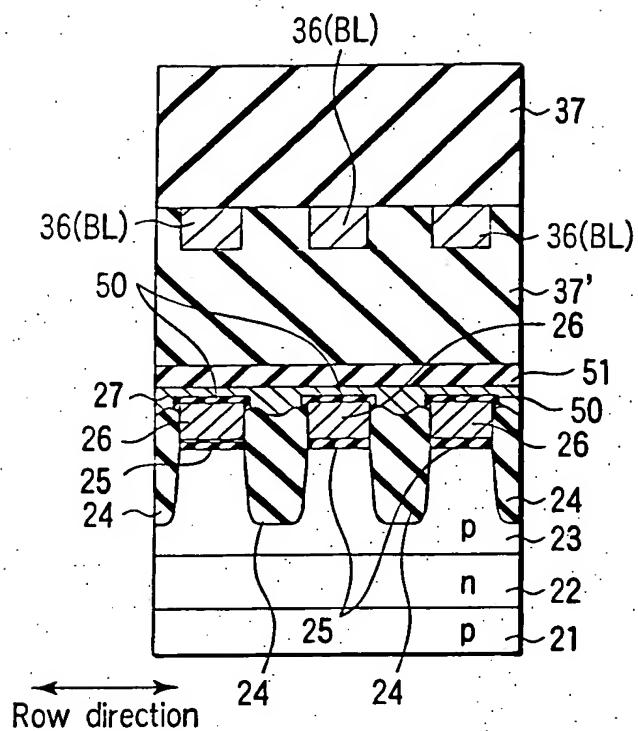


FIG. 32A

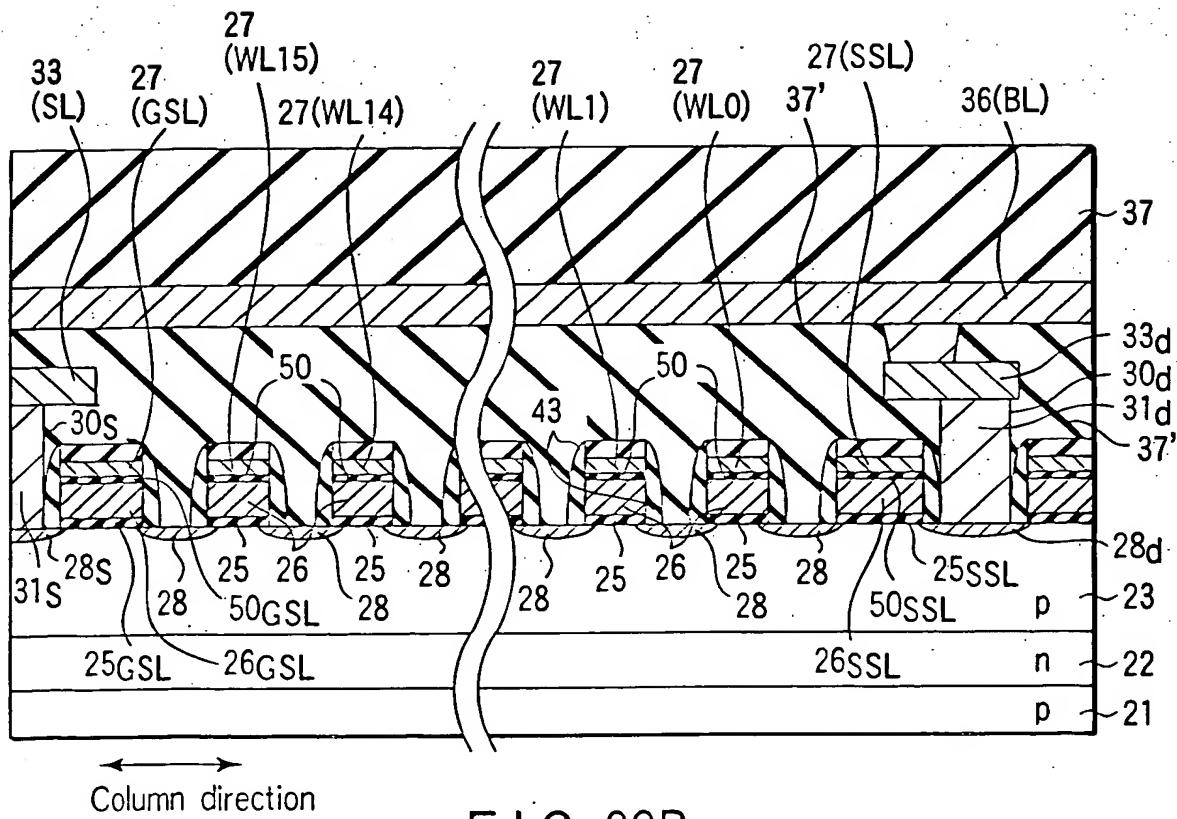


FIG. 32B

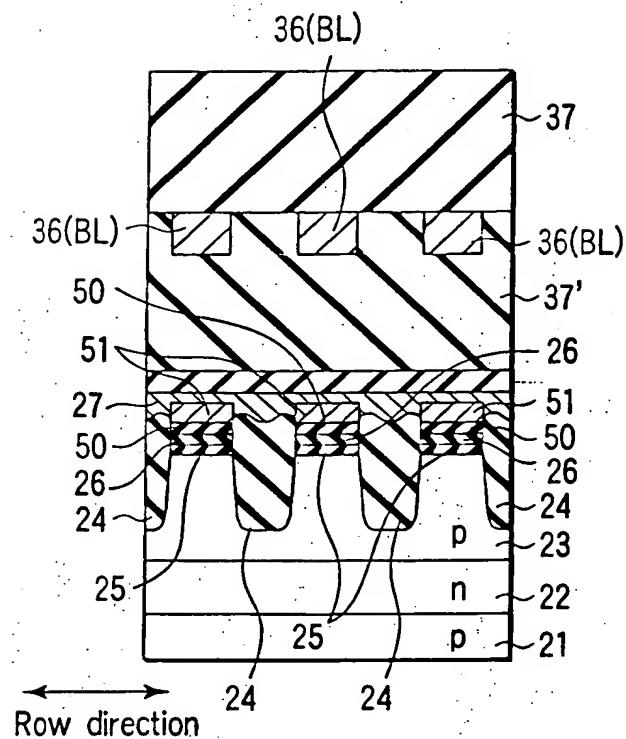


FIG. 33A

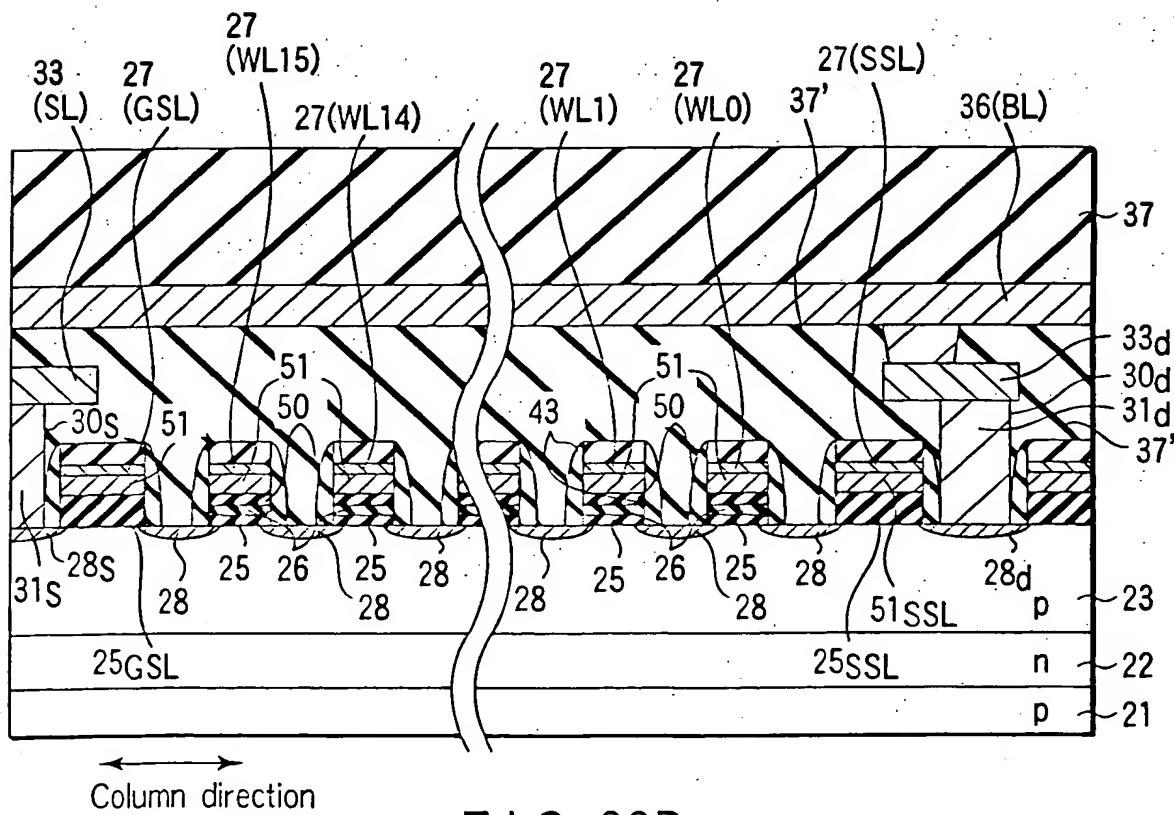


FIG. 33B

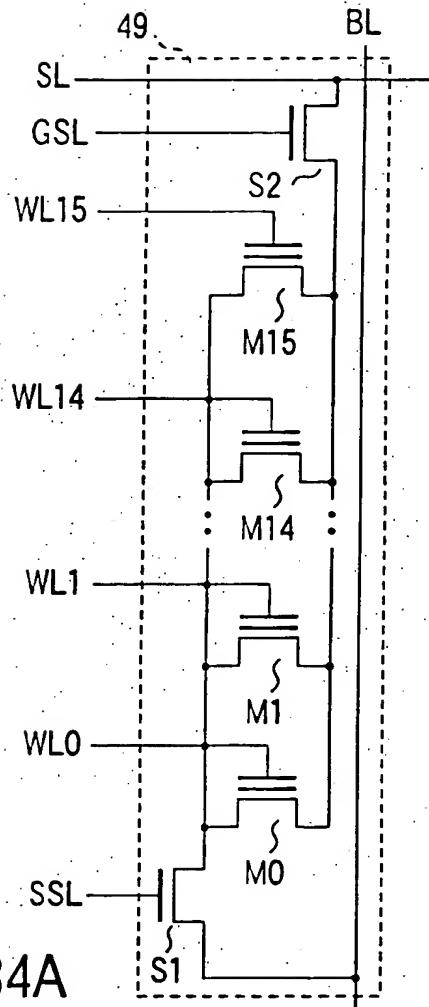


FIG. 34A

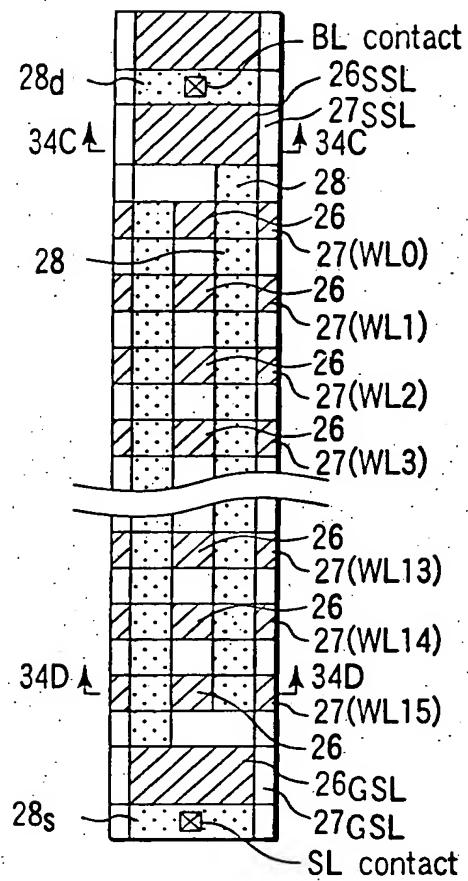


FIG. 34B

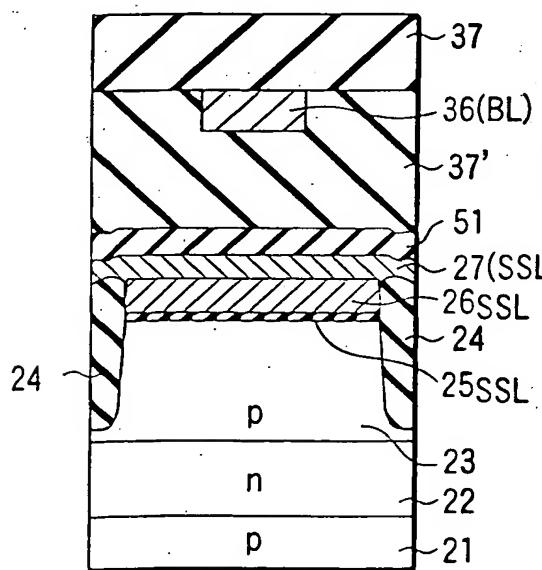


FIG. 34C

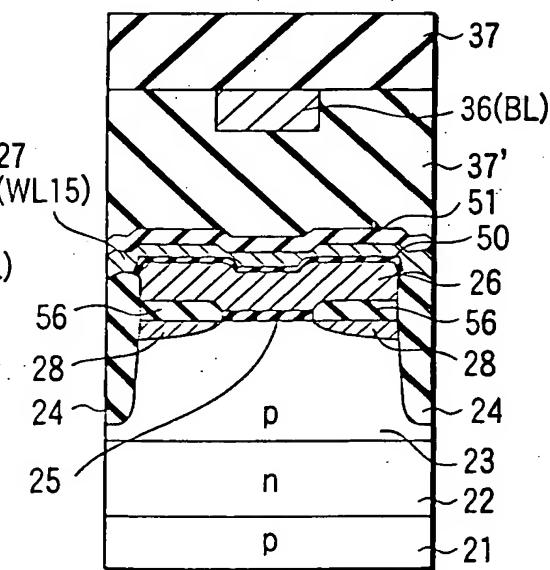


FIG. 34D

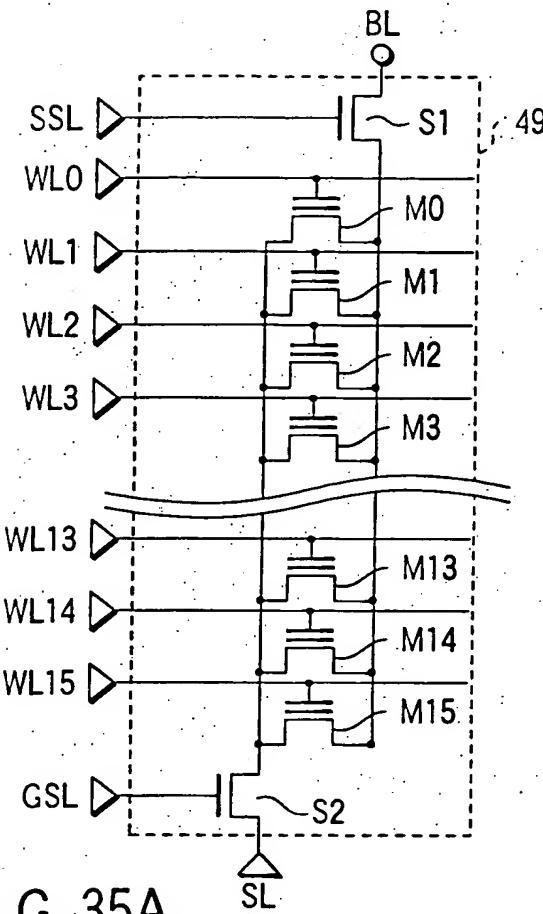


FIG. 35A

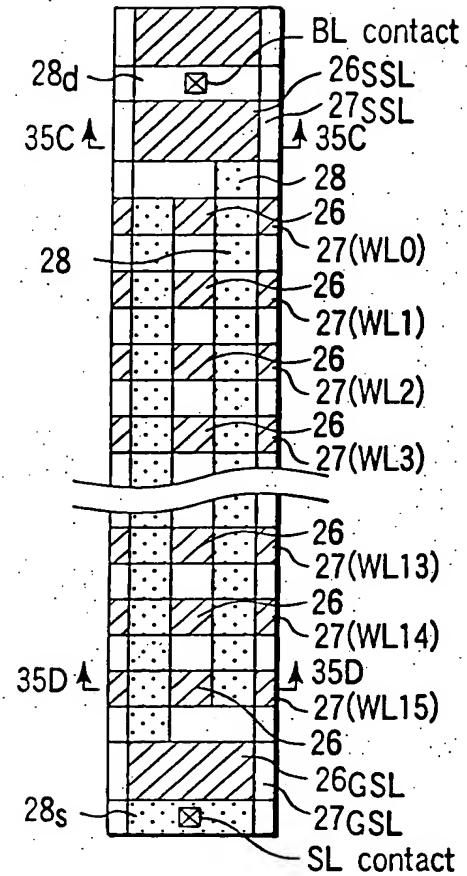


FIG. 35B

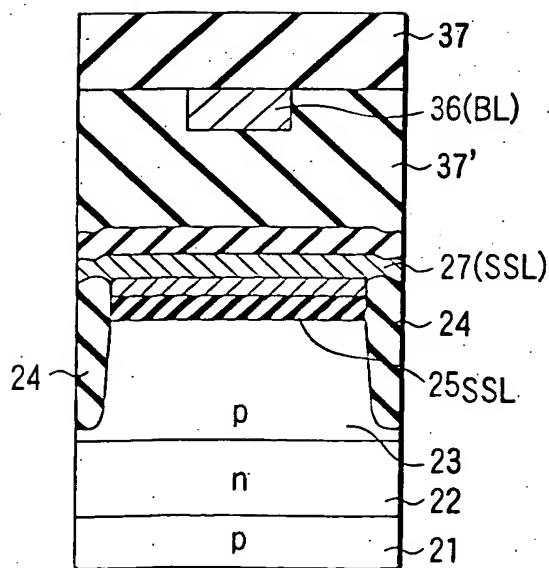


FIG. 35C

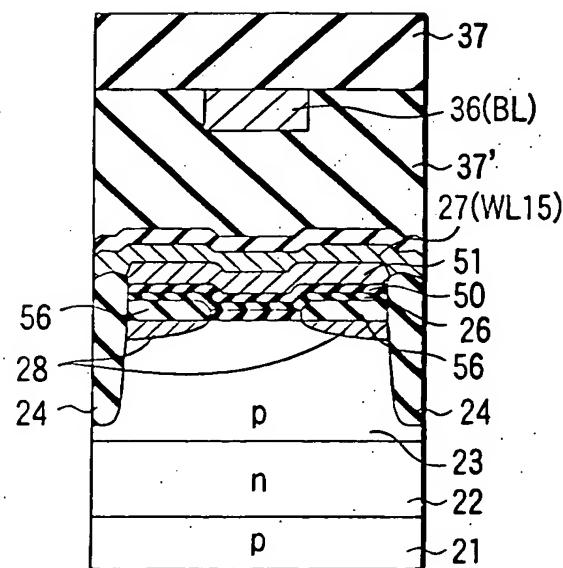


FIG. 35D

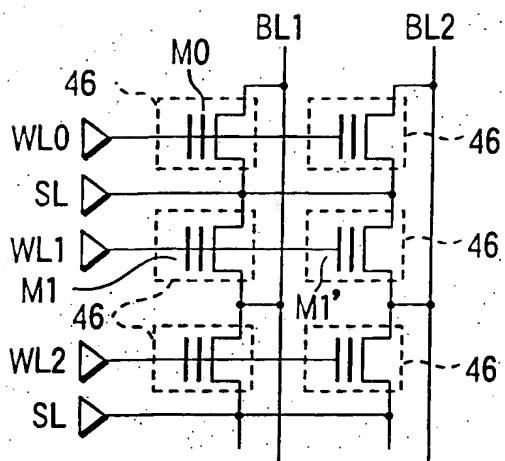


FIG. 36A

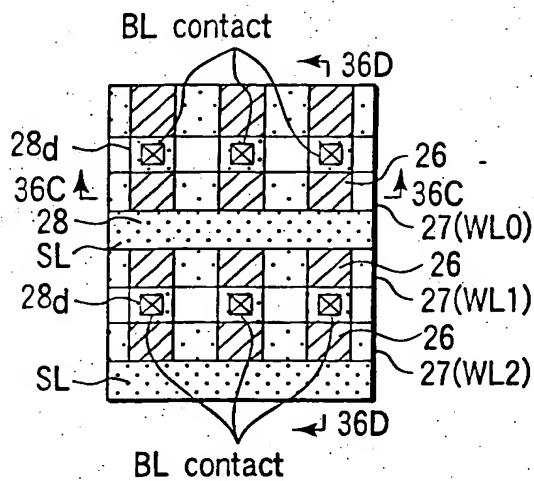


FIG. 36B

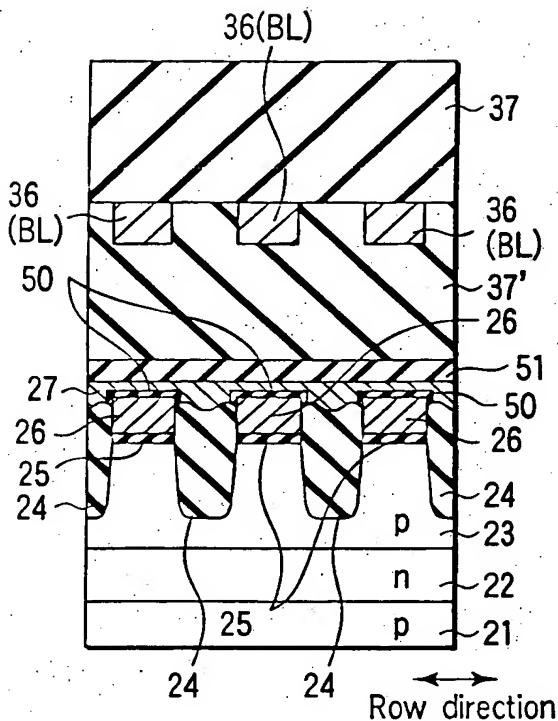


FIG. 36C

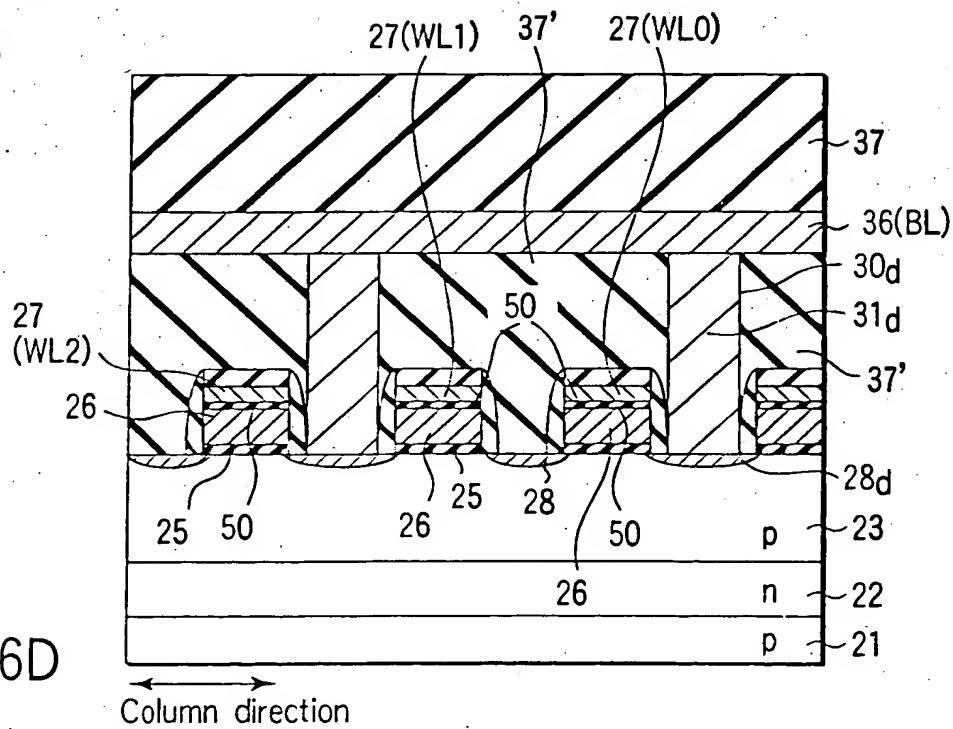


FIG. 36D

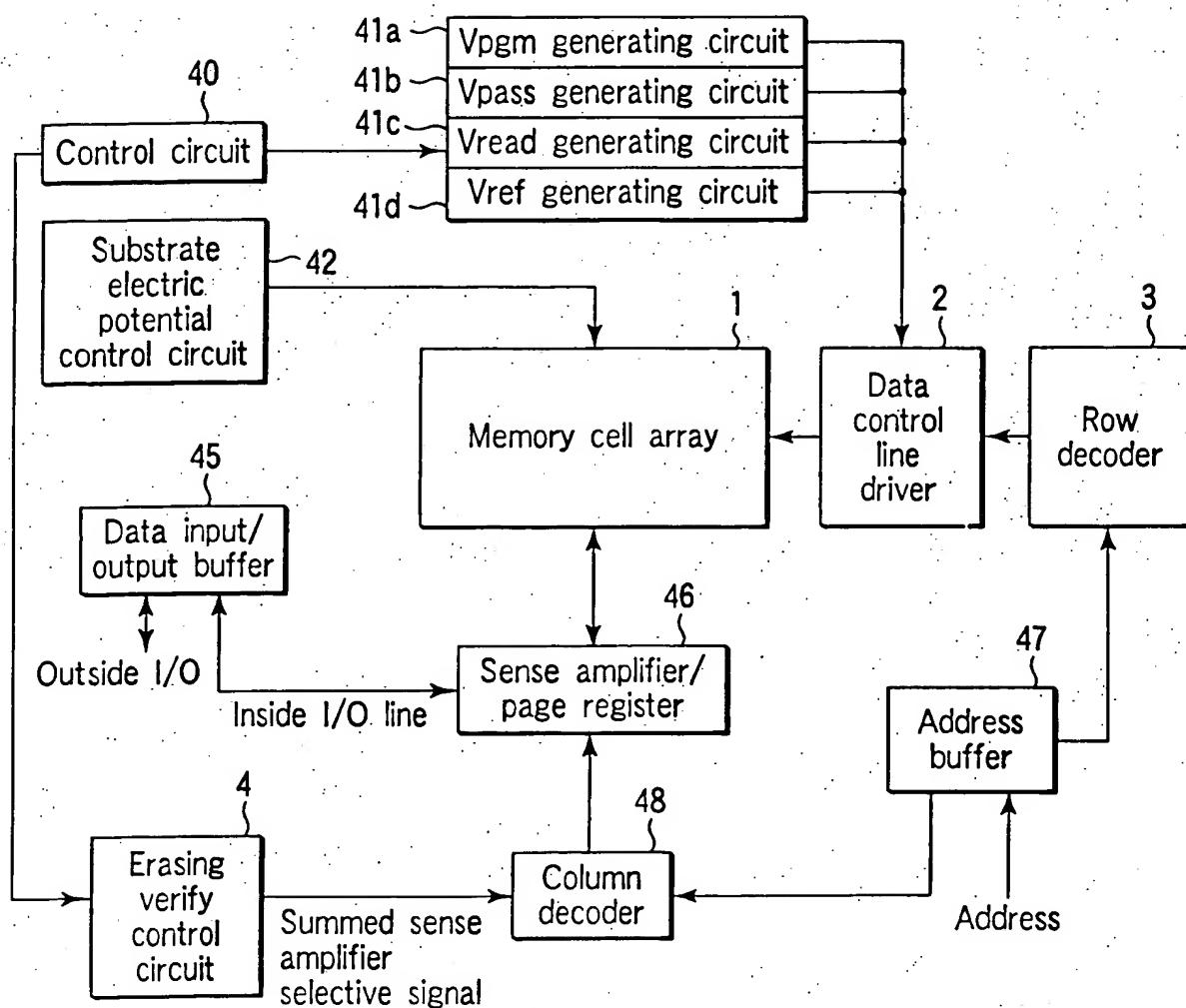


FIG. 37 PRIOR ART

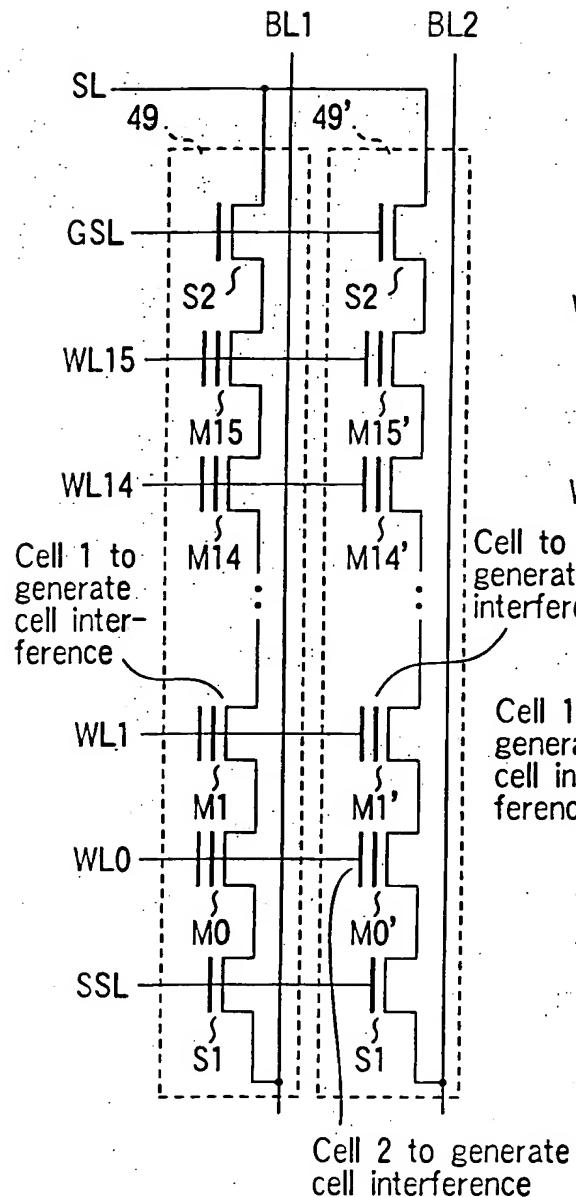


FIG. 38A  
 PRIOR ART

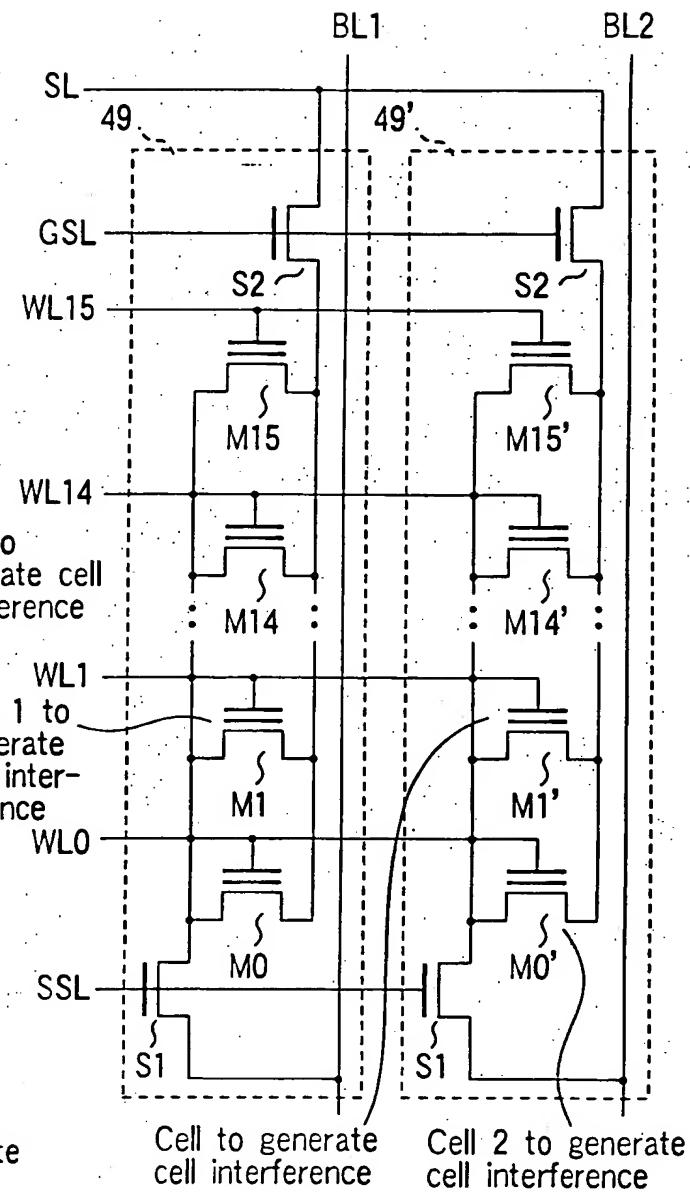


FIG. 38B  
 PRIOR ART

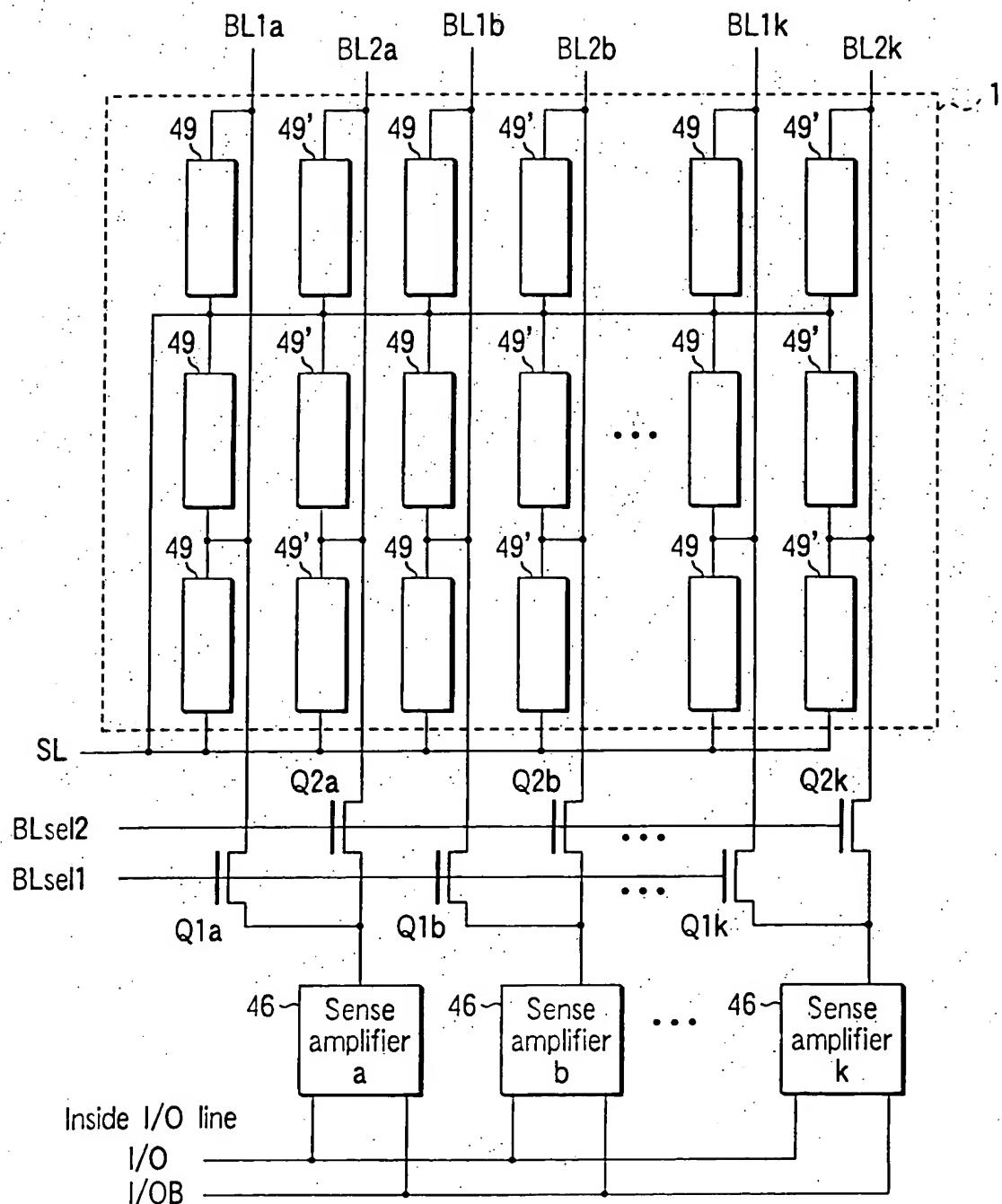


FIG. 39 PRIOR ART

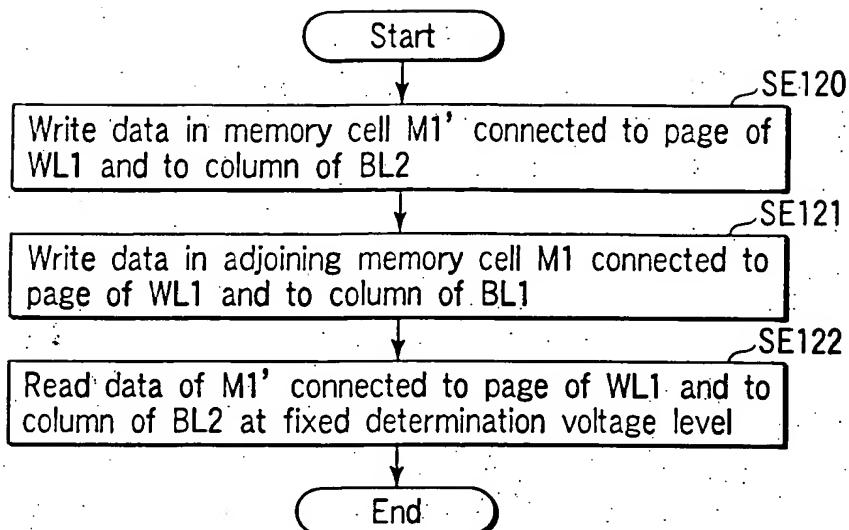


FIG. 40 PRIOR ART

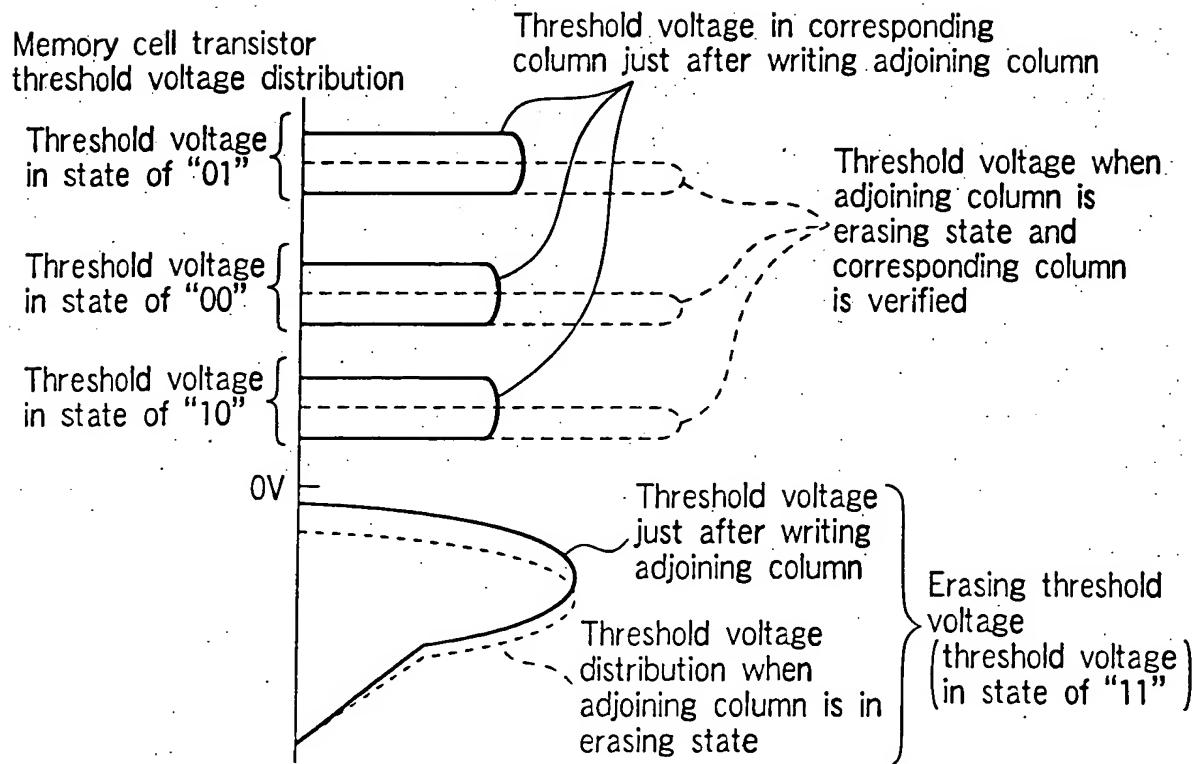


FIG. 41 PRIOR ART

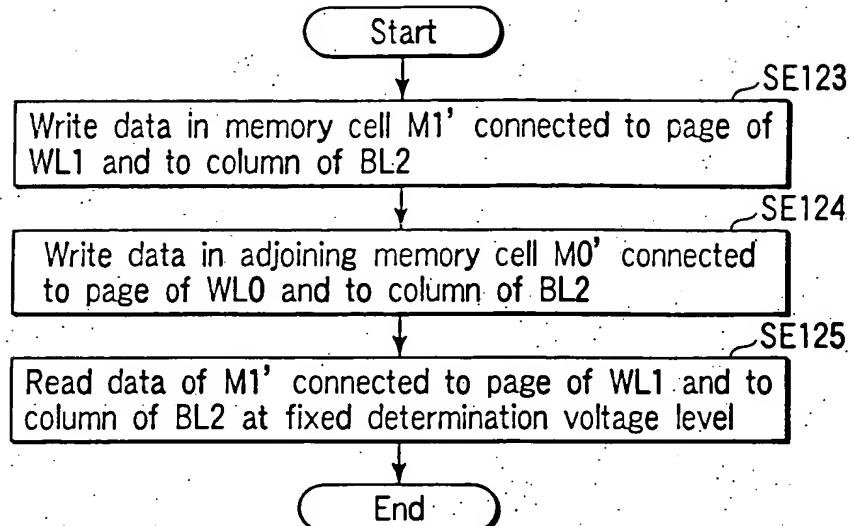


FIG. 42 PRIOR ART

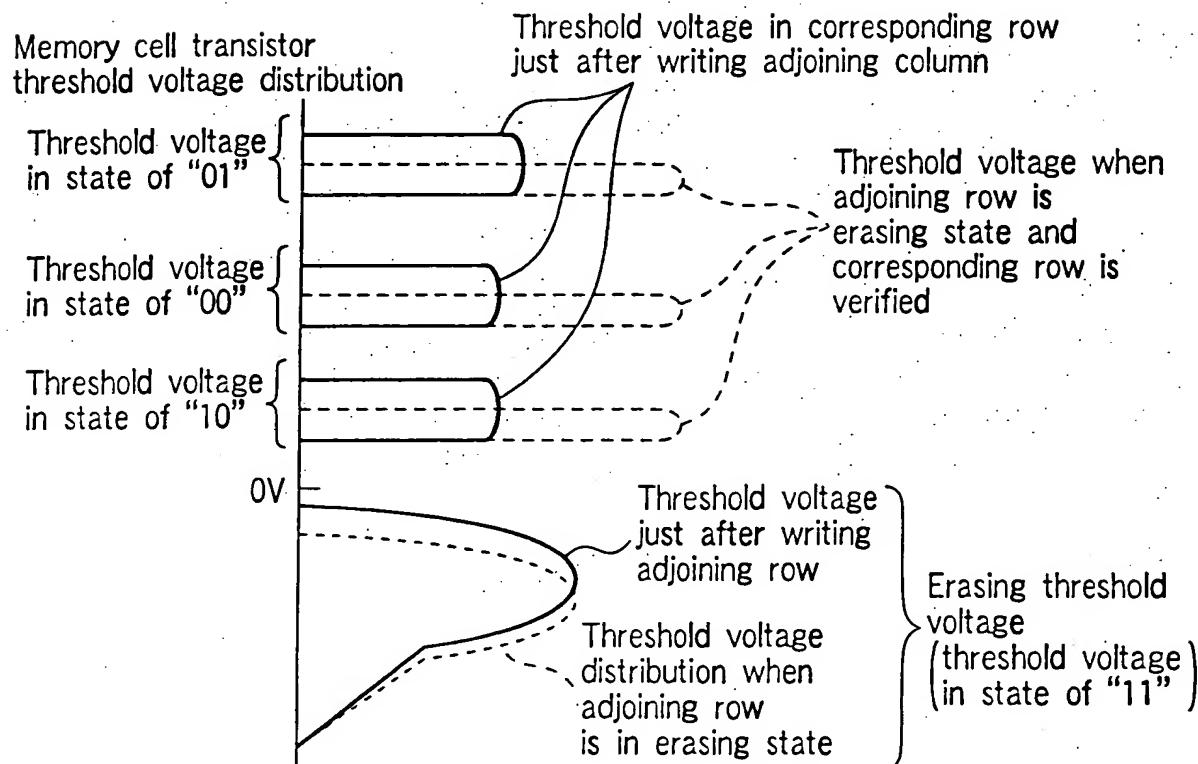


FIG. 43 PRIOR ART